

CRITERION -2 TEACHING-LEARNING AND EVALUATION

KEY INDICATOR 2.5- EVALUATION PROCESS AND REFORMS

**Metric No 2.5.1 Mechanism of Internal Assessment is Transparent and Robust
in Terms of Frequency and Mode**

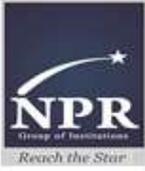
S.No.	Content	Page No.
Internal Assessment- Robust		
1	Internal Assessment Schedule	3
2	Internal Test circular	4
3	Internal Test Instructions	5
4	Question pattern	6
5	Question paper	8
6	Internal Test schedule	14
7	Hall Arrangement	17
8	Invigilation Duty list	21
9	Squad Duty list	23
10	Squad check list – Report	24
Internal Assessment- Transparent		
11.	Exam cell notice board	25
12.	Anna University Assessment Entry	26
13.	Students Absent Report to Parents	31

 <p>NPR Group of Institutions Reach the Star</p>	<p align="center">NPR College of Engineering & Technology NPR Nagar, Natham, Dindigul - 624401, Tamil Nadu, India. Approved by AICTE, New Delhi & Affiliated to Anna University, Chennai. An ISO 9001:2015 Certified Institution. Phone No: 04544- 246 500, 246501, 246502. Website : www.nprcolleges.org, www.nprcet.org, Email:nprcetprincipal@nprcolleges.org</p>	
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14.	Anna University Assessment-Students Login	32
15.	Anna University Result-Students Login	33
16.	Anna University Result Sent to the Parents through SMS	34
17.	ERP-Students login	35
18.	Sample reports of CIE	36
19.	Internal Assessment question Paper	37
20.	Answer Key	39
21.	Answer Sheet	43
22.	Model Practical Examination Schedule	54
23.	Model Practical Examination Question	61
24.	Record note	63
25.	Model Practical Examination Answer sheet	64
26.	Project Review schedule	73
27.	Project Review & Marks	76




PRINCIPAL
Dr. J.SUNDARARAJAN,
 B.E., M.Tech., Ph.D.,
 Principal
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INTERNAL ASSESSMENT- ROBUST

INTERNAL ASSESSMENT-SCHEDULE

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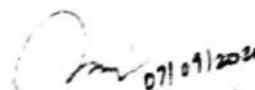
Internal Assessment Schedule for Non Autonomous Affiliated Institutions

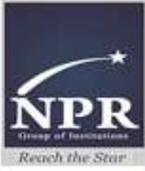
August - November 2020 (For all UG/PG - Programmes Except I Semester (Full Time/Part Time))

Report No	Report Period	Test Period	Report Entry Period
I	-----	----	-----
II	12-08-2020 -- 07-09-2020	01-09-2020 -- 05-09-2020	07-09-2020 -- 11-09-2020
III	08-09-2020 -- 30-09-2020	23-09-2020 -- 29-09-2020	30-09-2020 -- 06-10-2020
IV	01-10-2020 -- 26-10-2020	19-10-2020 -- 24-10-2020	26-10-2020 -- 28-10-2020

Saturdays may be included as working days to make good the Shortages, if any.



7.  07/09/2020
CONTROLLER OF EXAMINATIONS :



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INTERNAL TEST CIRCULAR

NPRCET/Exam Cell/2019-20/01

08.01.2020

CIRCULAR

All the Head of the department are instructed to check the quality of the internal test –I question papers of their respective departments. Please follow the below mentioned procedures and report to the undersigned.

- Allocation of questions must be based on bloom taxonomy.
- Submit the Question Bank for all the respective subjects within prescribed date.
- Instruct the faculty members to complete the evaluation process on the fourth day from the date of their respective test.
- Submit the internal and squad invigilators list to the COE section prior to the Internal test-I.

Principal

Principal

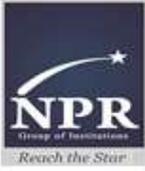
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Copy to

1. COE
2. All Hods/ Concerned Faculties
3. File

S.No	Name of the HoD	Department	Signature
1	Mr.V.MUTHUKUMAR	CIVIL	
2	Mr.J.VISWANATH	CSE	
3	Dr.A.GOPI SAMINATHAN	ECE	
4	Mr.G.ELANGO VAN	EEE	
5	Dr.T.MALAI CHAMY	MECH	
6	Dr.B.VELMURUGAN	MBA	
7	Dr.T.PRIYA	S&H	





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INTERNAL TEST INSTRUCTION

NPRCET/Exam Cell/2019-20/01

08.01.2020

EVEN SEMESTER (2019-20)-JANUARY 2020

FIRST INTERNAL ASSESSMENT TEST –INSTRUCTIONS

First Internal tests for all II and III years will be conducted from 21.01.2020(Tuesday) to 27.01.2020(Monday). IV years will be conducted from 30.01.2020(Thursday) to 04.02.2020(Tuesday). All the faculty members handling subject for III, V, and VII semester are requested to submit their question bank through the respective HoDs on or before 15.01.2020(Wednesday).

Syllabus: First Two Unit ONLY

Important instructions regarding question paper setting

- Faculty handling members are requested to submit the question bank for their respective subjects
- The file name of the question bank is to be saved as (year-semester-department-subject code and name)
- Example: IV-8 MECHANICAL-ME6016-Advanced I.C. Engines.
- Late submission of the question bank is not encouraged


Exam Cell Coordinator


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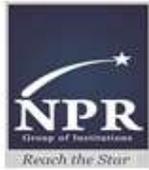
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Civil - V. Subramanian
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EEE - J. S. S. S.
ECE - J. S. S. S.
Mech - D. S. S. S.
SAH - T. R. S. S.
MBA - S. S. S. S.



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QUESTION PATTERN

Register Number:

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Department ofEngineering

INTERNAL TEST- I / II

Subject code & Name:

Year &Semester:

Duration: 90 Minutes

Date:

Maximum marks: 50

Course Outcomes ,Question Number, Marks:

COs	CO1	CO2
Ques. No.		
Max. Marks		

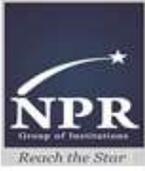
COs & K-Level:

CO1:

CO2:

K-Levels	K1: Remember	K2: Understand	K3:Apply	K4:Analyse	K5:Evaluate	K6:Create
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Q.No	Answer All Questions	Bloom's Taxonomy K-level
PART – A (5 x 2 = 10 marks)		
1.		
2.		



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3.			
4.			
5.			
PART – B (1 x 8 = 8 marks)			
6.		(8)	
PART – C (2 x16 = 32 marks)			
7.		(16)	
8.	(i)	(8)	
	(ii)	(8)	

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Note: In Part C, questions can be set for 16 marks if the answer is very lengthy otherwise two or more sub-divisions can be included in each full question with break-up of marks indicated in brackets.





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INTERNAL TEST -I QUESTION PAPER

Register Number:

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INTERNAL EXAMINATION I – AUGUST 2019

5th Semester – CSE

CS8591-Computer Networks

Duration: 90 Minutes

Date: 5/08/2019

Maximum marks: 50

Course Outcomes ,Question Number, Marks:

COs	CO1	CO2
Ques. No.	1,2,8	3,4,5,6,7
Max. Marks	20	30

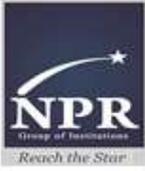
COs & K-Level:

CO1: Students can be able to **Identify** various layers of network and discuss the functions of physical layer

CO2: Students will be able to **Discuss** how data flows from one node to another node with regard to data link layer

K-Levels	K1: Remember	K2: Understand	K3:Apply	K4:Analyse	K5:Evaluate	K6:Create
----------	--------------	----------------	----------	------------	-------------	-----------

Q.No	Answer All Questions	Bloom's Taxonomy K-level
PART – A (5 x 2 = 10 marks)		
1.	Generalize LAN, WAN and MAN.	K1
2.	Define: Transmission media and its types.	K1
3.	Define the term Bandwidth and Latency.	K1
4.	Discuss the Unicast, Multicast, and Broadcast Addresses.	K2
5.	What is meant by Framing?	K2



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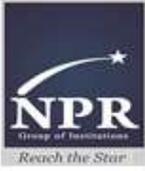
PART - B (1 x 8 = 8 marks)		
6.	Explain about network and its types.	K2
PART - C (2 x 16 = 32 marks)		
7.	Explain in detail about switching and its types. 1.Circuit switching 2.Packet Switching	K6
8.	Explain in detail about the data link layer noisy protocols with neat diagram.	K6

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Note: In Part C, questions can be set for 16 marks if the answer is very lengthy otherwise two or more sub-divisions can be included in each full question with break-up of marks indicated in brackets.





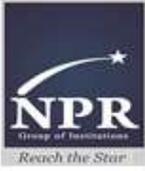
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PART – A (10x 2 = 20 marks)		
1.	List the parameters used to measure the performance of computer networks?	K1
2.	Define the terms: Bandwidth, Latency.	K1
3.	Define flow control.	K1
4.	What is meant by bit stuffing? Give an example.	K1
5.	Differentiate between forwarding table and routing table.	K1
6.	What is multicasting?	K1
7.	Differentiate between TCP and UDP.	K1
8.	What is meant by socket address?	K1
9.	Define URL.	K1
10.	State the difference between SMTP and MIME.	K1
PART – B (5 x 13 = 65 marks)		
11.(a)	Draw the OSI model network architecture with neat diagram & Explain the functionalities of each layer in detail.	K2
OR		
11.(b)	Explain in detail about transmission media and its types with neat diagram.	K2
OR		
12.(a)	i) Explain about data link layer noiseless protocols.	K2
	ii) Write short notes on HDLC frame format.	K1
OR		
12.(b)	i) Explain about CSMA in detail	K2
	ii) Show and explain about Ethernet frame format.	K5



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13.(a)	Explain in detail about network layer protocols with a neat diagram.	K2
OR		
13.(b)	Explain in detail about the working procedure of RIP, OSPF and BGP with neat diagram.	K2
OR		
14.(a)	Explain in detail about the transport layer protocols with neat diagram.	K2
OR		
14.(b)	How is congestion controlled? Explain in detail about congestion control techniques in transport layer.	K4
OR		
15.(a)	Explain in detail about WWW and HTTP techniques.	K2
OR		
15.(b)	Discuss the working procedure of E-MAIL in detail.	K6
PART – C (1 x15 = 15 marks)		
16(a)	Illustrate the sequence of events and the respective protocols involved while accessing a web page from a machine when it is connected with the internet for first time.	K5
OR		
16(b)	i) Draw the format of TCP header and explain each of its field.	K2
	ii) Specify the justification for having variable length for the fields in the TCP.	K4

V. Smt
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Note:

1. In Part B, questions can be set for 13 marks if the answer is very lengthy otherwise two or more sub-divisions can be included in each full question with break-up of marks indicated in brackets.
2. In Part C, questions can be set for 15 marks if the answer is very lengthy otherwise two or more sub-divisions can be included in each full question with break-up of marks indicated in brackets.





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INTERNAL TEST SCHEDULE

B.E - INTERNAL TEST - I - JAN- 2020

TIME: 11.15 AM – 12.45 PM

TIME TABLE

DATE: 08.01.2020

DATE	IV SEMESTER				
	CIVIL	CSE	EEE	ECE	MECH
21.01.2020 (Tuesday)	MA8491 Numerical Methods	CS8451 Design and Analysis of Algorithms	MA8491- Numerical Methods	MA8451 Probability and Random Processes	MA8452 Statistics and Numerical Methods
22.01.2020 (Wednesday)	CE8401 Construction Techniques and Practices	MA8402 Probability and Queueing Theory	EE8401- Electrical Machines - II	EC8451 Electromagneti c Fields	ME8492 Kinematics of Machinery
23.01.2020 (Thursday)	CE8402 Strength of Materials II	CS8492 Database Management Systems	EE8402- Transmission and Distribution	EC8491 Communicatio n Theory	ME8451 Manufacturing Technology – II
24.01.2020 (Friday)	CE8403 Applied Hydraulic Engineering	CS8493 Operating Systems	IC8451- Control Systems	EC8453 linear Integrated Circuits	ME8491 Engineering Metallurgy
25.01.2020 (Saturday)	CE8404 Concrete Technology	CS8494 Software Engineering	EE8451- Linear Integrated Circuits and Applications	EC8452 Electronic Circuits II	CE8395 Strength of Materials for Mechanical Engineers
27.01.2020 (Monday)	CE8491 Soil Mechanics	CS8491 Computer Architecture	EE8403- Measurements and Instrumentation	GE8291 Environmental Science and Engineering	ME8493 Thermal Engineering - I



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DATE	VI SEMESTER				
	CIVIL	CSE	EEE	ECE	MECH
21.01.2020 (Tuesday)	CE8601 -Design of Steel Structural Elements	CS8651- Internet Programming	EE8691- Embedded Systems	EC8652 - Wireless Communication	ME8651 -Design of Transmission Systems
22.01.2020 (Wednesday)	CE8602 - Structural Analysis II	CS8601- Mobile Computing	EE8601- Solid State Drives	EC8651- Transmission lines and RF systems	ME8691 -Computer Aided Design and Manufacturing
23.01.2020 (Thursday)	CE8604- Highway Engineering	CS8691- Artificial Intelligence	EE8602- Protection and Switchgear	EC8004- Wireless Networks	ME8693- Heat and Mass Transfer
24.01.2020 (Friday)	CE8603- Irrigation Engineering	CS8602- Compiler Design	EE8005- Special Electrical Machines	EC8095- VLSI Design	ME8692 -Finite Element Analysis
25.01.2020 (Saturday)	EN8592 -Waste water Supply Engineering	CS8603- Distributed Systems	EE8004- Modern Power Converters	EC8691- Microprocessor and Microcontroller	ME8694 -Hydraulics and Pneumatics
27.01.2020 (Monday)	CE8001- Ground Improvement Techniques	IT8076 - Software Testing	-	MG8591- Principles of Management	PR8592 -Welding Technology



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DATE	VIII SEMESTER				
	CIVIL	CSE	EEE	ECE	MECH
30.01.2020 (Thursday)	MG6851 Principles of Management	CS6801 Multi – Core Architectures and Programming	EE6010- High Voltage Direct Current Transmission	EC6802 Wireless Networks	MG6863 Engineering Economics
31.01.2020 (Friday)	CE6016 Prefabricated Structures	GE6075 Professional Ethics in Engineering	EE6801- Electric Energy Generation, Utilization and Conservation	EC6801 Wireless Communication	IE6605 Production Planning and Control
03.02.2020 (Monday)	CE6021 Repair and Rehabilitation of Structures	CS6008 Human Computer Interaction	GE6075- Professional Ethics in Engineering	GE6757 Total Quality Management	ME6016 Advanced I.C. Engines / ME6019 Non- Destructive Testing and Materials
04.02.2020 (Tuesday)	----	----	----	CS6003 Ad hoc and Sensors Networks	----


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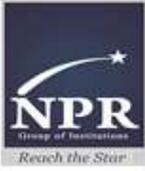
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EXAMINATION – HALL ARRANGEMENT

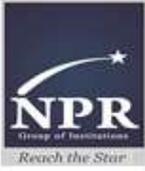
ACADEMIC YEAR 2019-2020 DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING HALL PLAN

HALL NO - MBLH207

COLUMN 1	COLUMN 2	COLUMN 3	COLUMN 4	COLUMN 5
920818104002 ARAVIND B	920816104003 ASHWIN SARAVANAN.R.C	920818104007 GUNA DEVI I	920816104008 DIVYA.A	920818104012 KARTHIGA JOTHI S
920817104001 AARTHI R	920818104004 CHANDRU R	920817104005 AISHWARYA R (12.12.99)	920818104010 KAMALI B	920817104009 AMRUTHAA SHREE R
920816104001 ABIRAMI.M	920817104003 AISHWARYA A	920816104005 BALAJI.R.M	920817104007 AJITH KUMAR P J K	920816104010 GOMATHIAKILA.D
920818104003 BHAVITHRA R	920816104004 BALAJI.J	920818104009 HEMAPRIYA M	920816104009 DURGADEVI.P	920818104017 MUNEESHWARAN N
920817104002 ABIRAMI M	920818104005 CHITRA B	920817104006 AISHWERYA SRI R	920818104011 KANMANI A	920817104010 ARUNKUMAR S
920816104002 ARUNA DEVI.P	920817104004 AISHWARYA R(16.9.99)	920816104006 CHARUMATHY.B	920817104008 ALAGURAJA R	920816104011 GURU PRASATH.S




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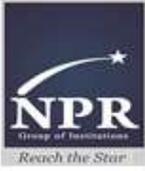
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HALL PLAN

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920817104011 BONSIYA R	920818104021 NIVETHA A	920817104017 GURUPRABU R	920818104026 RAMYA R	920817104021 KAVIYA B I
920816104012 HARIVIGESH.S	920817104013 DEV M	920816104016 KARTHIGA.S	920817104019 HARI PRASATH T	920816104020 MAHALAKSHMI.M
920818104020 NIVEDHA R	920816104015 KARTHICKRAJA.T	920818104024 PRAKASH P	920816104019 MADHUMITHA.A	920818104029 SAFRINE BANU S
920817104012 CHELLAPANDIAN R	920818104022 PALPANDI R	920817104018 GURURAJ B	920818104027 ROOBICA N	920817104022 KAVIYARASAN A
920816104013 ILAMATHLG	920817104015 DINESH KUMAR C	920816104017 KEERTHANA.M	920817104020 JEGANATHAN R	920816104021 MOINUDEEN.S




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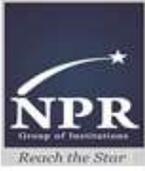
ACADEMIC YEAR 2019-2020
DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING
HALL PLAN

HALL NO - MBLH210

COLUMN 1	COLUMN 2	COLUMN 3	COLUMN 4	COLUMN 5
920818104030 SANTHOSHINI V	920816104024 PAVITHRAN.M	920818104035 SHAHITHA RIZWANA S	920816104028 PREETHA.T	920818104039 VINEETH PANDIAN S
920817104023 KEERTHANA G	920818104033 SARANIYA.M	920817104027 MADHUBALA S	920818104037 SRIDHAR K	920817104032 NAGAJOTHI C
920816104022 MUTHU.T	920817104025 KOUSHIK ROMEL J	920816104026 PRAVINVINO.M	920817104029 MANOJ S	920816104030 SANTHOSHKUMAR.A
920818104031 SANTHOSHKUMAR T	920816104025 PRASATH.T	920818104036 SHRINITHI M	920816104029 SABARIKRISHNAN S.K	920818104040 YOGESHWARAN V
920817104024 KODEESWARAN S	920818104034 SATHANA S	920817104028 MANOJ K	920818104038 VIJAYA BHARATHI P	920817104034 NATHIPRIYA K
920816104023 NANDHINI.S	920817104026 LOGESHWARI S	920816104027 PREETHA.B	920817104030 MANUSHA M	920816104031 SELVANAYAKI.C




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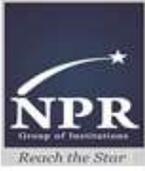
ACADEMIC YEAR 2019-2020
DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING
HALL PLAN

HALL NO - MBLH212

COLUMN 1	COLUMN 2	COLUMN 3	COLUMN 4	COLUMN 5
920817104035 NATHISHKUMAR R	920816104035 SHRUTHI.M	920817104042 PRAKASH KUMAR S	920816104044 YOGESHJOHN.K	920817104049 SUBASRI S
920816104032 SENTHIL.N.P	920817104039 PARAMESHWARI P	920816104040 SUNDARAM.S.K.K	920817104046 SANGEERANI P	920816104702 NAVRIN BAJILA
920817104036 NAVIN MANIKANDAN S	920816104036 SIVASUBRAMANIAN.C	920817104043 PREETHI A	920816104041 SURYA.S	920817104051 VARSHA M
920816104033 SHREEDHAR.V	920817104040 POOVARASAN M	920816104042 SWETHA.A	920817104047 SASTIANANDHAN M	920817104052 VIJAYALAKSHMI S
920817104037 NELSON J	920816104038 SRINITHI.P	920817104044 RAJADURAI R	920816104301 SHARUKKHAN.S	920817104053 VIJAYA SHANTHI M
920816104034 SHRIJANANI.S	920817104041 POOVENDRAN K	920816104043 TAMIL SELVAN.M	920817104048 SHARMA A	920817104054 VIJAYKRISHNA T
920817104038 NITHIS KANNAN G	920816104039 SRIPRIYA.P	920817104045 ROFINA BEGAM N	920816104701 D.KISHOREKUMAR	920817104055 YOGESH KANNAN S
---	---	--	----	920817104056 YOGESHVARAN MV



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INVIGILATION DUTY LIST

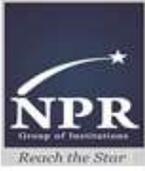
FIRST INTERNAL EXAMINATION FOR HIGHER SEMESTER -JAN/FEB 2020

INTERNAL INVIGILATOR DUTY LIST

Date: 25.01.2020

Sl.No	Name of the Staff	Designation	Date			Signature
1	Dr.T.Priya	Prof / Maths	21.01.2020	----	----	
2	Mrs.A.Kanimozhi	AP /Maths	22.01.2020	----	----	
3	Ms.S.Kanaga Lakshmi	AP / Maths	23.01.2020	----	----	
4	Mrs.K.Kavitha	AP / Eng	24.01.2020	----	----	
5	Ms.R.Sindhu	AP / Eng	25.01.2020	----	----	
6	Mr.K.Selvam	AP / Civil	21.01.2020	23.01.2020	'----	
7	Mr.C.Vijaya Kumar	AP / Civil	21.01.2020	'----	'----	
8	Mr.P.Manikandan	AP / Civil	21.01.2020	----	25.01.2020	
9	Mr.N.Karthic	AP /Civil	'----	24.01.2020	27.01.2020	
10	Ms.E.Madhu Mathi Suriya	AP /Civil	'----	24.01.2020	27.01.2020	
11	Mrs.S.Prema Sundari	AP / Civil	23.01.2020	24.01.2020	27.01.2020	
12	A.Saranya Jenifer	AP / Civil	22.01.2020	'----	25.01.2020	
13	V.Prakash	AP / Civil	22.01.2020	----	25.01.2020	
14	T. K .Prasanna	AP / Civil	'----	23.01.2020	'----	
15	S.Darwin	AP / Civil	'----	23.01.2020	'----	
16	Mr.J.Dinesh	AP / CSE	21.01.2020	'----	25.01.2020	
17	Mr.J.Viswanath	AP / CSE	21.01.2020	'----	25.01.2020	
18	Mrs.V.Sujitha	AP / CSE	'----	23.01.2020	25.01.2020	
19	Ms.J.Bama	AP / CSE	'----	24.01.2020	'----	
20	Mrs.J.Prisca Mary	AP /CSE	22.01.2020	24.01.2020	'----	
21	P.Kaleeshwari	AP /CSE	21.01.2020	23.01.2020	'----	
22	J.Prisca Mary	AP /CSE	22.01.2020	'----	27.01.2020	
23	R.Sugashini	AP /CSE	----	23.01.2020	27.01.2020	
24	Mr.M.Pandi Maharajan	AP / EEE	21.01.2020	23.01.2020	25.01.2020	
25	Mrs.S.T Saranya	AP / EEE	'----	23.01.2020	'----	
26	Mr.M.Edwin Lawrence	AP / EEE	21.01.2020	23.01.2020	25.01.2020	





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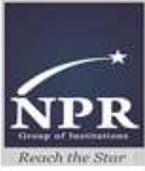
27	Ms.R.Poornima	AP /EEE	22.01.2020	24.01.2020	27.01.2020	<i>[Signature]</i>
28	Mr.T.Sivakumar	AP /EEE	22.01.2020	'....	27.01.2020	<i>[Signature]</i>
29	M.Thangapandian	AP /EEE	21.01.2020	24.01.2020	25.01.2020	<i>[Signature]</i>
30	Mrs.T.Esther	AP /ECE	-	23.01.2020	-	<i>[Signature]</i>
31	Mr.J.G.Sabarish	AP /ECE	21.01.2020	-	-	<i>[Signature]</i>
32	Mr.S.Surendhar	AP /ECE	-	23.01.2020	-	<i>[Signature]</i>
33	Ms.S.Monika	AP /ECE	-	-	27.01.2020	<i>[Signature]</i>
34	Mrs.C.Kannika Parameshwari	AP /ECE	-	24.01.2020	-	<i>[Signature]</i>
35	S.Vinayagam	AP /ECE	21.01.2020	-	25.01.2020	<i>[Signature]</i>
36	R.Yogesh	AP /ECE	22.01.2020	-	25.01.2020	<i>[Signature]</i>
37	A.Iniya Mary	AP /ECE	21.01.2020	-	-	<i>[Signature]</i>
38	S.Jothi Manikandan	AP /ECE	22.01.2020	-	25.01.2020	<i>[Signature]</i>
39	K.Sundarapandi	AP /ECE	-	-	27.01.2020	<i>[Signature]</i>
40	Dr.T.Malaichamy	AP /Mech	21.01.2020	25.01.2020	---	<i>[Signature]</i>
41	Mr.S.Suresh Kumar	AP /Mech	21.01.2020	23.01.2020	-	<i>[Signature]</i>
42	Mr.G.Sethuraj	AP /Mech	21.01.2020	23.01.2020	25.01.2020	<i>[Signature]</i>
43	Mr.G.Sundararajan	AP /Mech	21.01.2020	24.01.2020	27.01.2020	<i>[Signature]</i>
44	Mr.P.Thangamuniyappan	AP /Mech	22.01.2020	24.01.2020	27.01.2020	<i>[Signature]</i>
45	Mr.T.Balasubramani	AP /Mech	22.01.2020	24.01.2020	27.01.2020	<i>[Signature]</i>
46	Mr.Thangapandian	AP /Mech	22.01.2020	24.01.2020	27.01.2020	<i>[Signature]</i>
47	Mr.M.Mathan Raj	AP /Mech	22.01.2020	24.01.2020	27.01.2020	<i>[Signature]</i>
48	Mr.R.P.Thirumurugan	AP /Mech	22.01.2020	24.01.2020	27.01.2020	<i>[Signature]</i>
49	Mr.B.Deepan	AP /Mech	22.01.2020	24.01.2020	27.01.2020	<i>[Signature]</i>
50	Mr.R.Narayanan	AP /Mech	22.01.2020	23.01.2020	25.01.2020	<i>[Signature]</i>
51	Dr.S.Paul Singarayyar	AP /Mech	22.01.2020	27.01.2020	---	<i>[Signature]</i>
52	G.Vinoth Kumar	AP /Mech	21.01.2020	24.01.2020	-	<i>[Signature]</i>
53	M.Vimalchand	AP /Mech	-	23.01.2020	25.01.2020	<i>[Signature]</i>

[Signature]
Exam cell coordinator

[Signature]
Principal
(Dr.J.SUNDARARAJAN)



Dr. J.SUNDARARAJAN,
B.E., M.Tech., Ph.D.,
Principal
N.P.R. College of Engineering & Technology
Natham, Dindigul (Dt) - 624 401.



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INTERNAL TEST SQUAD DUTY LIST

FIRST INTERNAL EXAMINATION FOR HIGHER SEMESTER -JAN/FEB 2020

SQUAD DUTY LIST

Sl.No	Name of the Staff	Designation	Dept	Sign
1	Mr.K.Karuppasamy	AP / Civil	ECE	
2	Mr.G.Nagaraj	AP / CSE	MECH	
3	Mr.S.Sathyamoorthi	AP / EEE	CSE	
4	Mr.K.Jaya Prakasam	AP / ECE	EEE	
5	Mr.K.Aruna Senthil Kumar	AP / Mech	Civil	

Exam cell coordinator

Principal

Dr. J.SUNDARARAJAN,
B.E., M.Tech., Ph.D.,
Principal
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INTERNAL TEST SQUAD REPORT

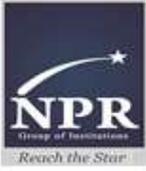
Squad Duty Report

Staff Name: *Mr. S. Sathyamoorthi*
Date: *24-01-2020*

Designation/Department: *AP/EEE*

S.No	Exam Hall No	Supervisor		Furniture & Seating arrangements		Allotted Invigilators present		Whether the Supervision was effective		Answer sheets were duly Verified by the Invigilators		Malpractice Identified.		Remarks
		Name	Designation /Department	Yes	No	Yes	No	Yes	No	Yes	No	Yes	No	
1	MSLH 207	Mr. K. Karthik	AP/Civild	✓		✓		✓		✓			✓	
2	MSLH 209	Mrs. K. Karthik	AP/English	✓		✓		✓		✓			✓	
3	MSLH 210	Mr. M. Mahesh	AP/Mech	✓		✓		✓		✓			✓	
4	MSLH 212	Mr. B. Deepan	AP/Mech	✓		✓		✓		✓			✓	
5														





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If Malpractice Identified.

S.No	Student Name	Department	Year/Sec	Type of Malpractice	Action Taken	Remarks

Signature of Squad

Examcell

Principal

Dr. J.SUNDARARAJAN,

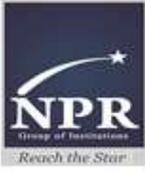
B.E., M.Tech., Ph.D

Principal

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Natham, Dindigul (Dt) - 624 401





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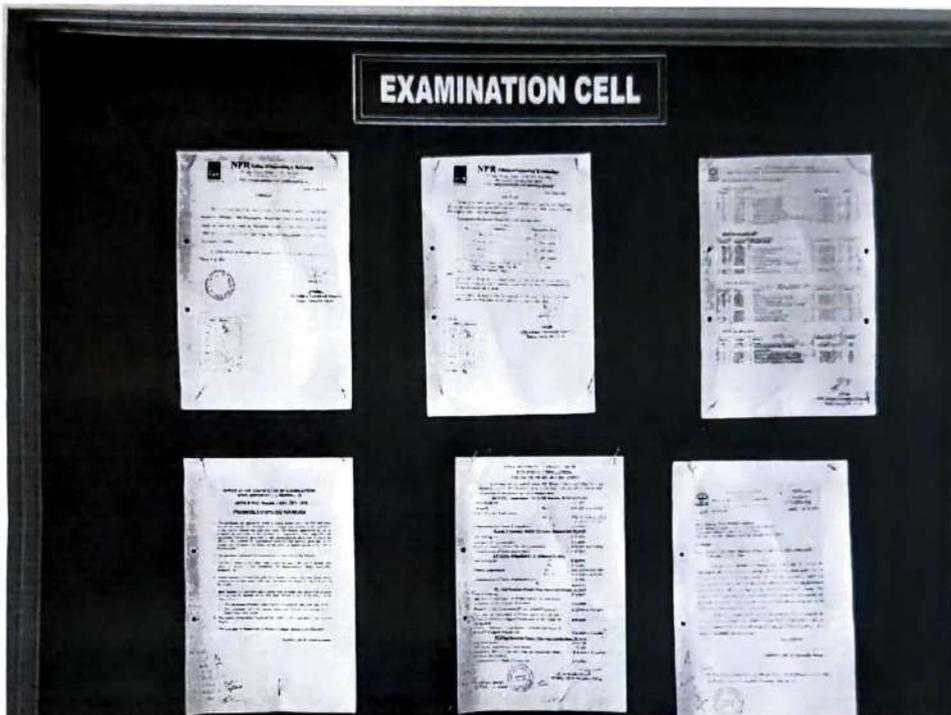
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INTERNAL ASSESSMENT- TRANSPARENT NOTIFICATION OF CIE DISPLAYED IN NOTICE BOARD

Exam Cell Notice Board



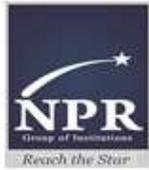
	<p>NPR College of Engineering & Technology NPR Nagar, Natham, Dindigul - 624401, Tamil Nadu, India. Approved by AICTE, New Delhi & Affiliated to Anna University, Chennai. An ISO 9001:2015 Certified Institution. Phone No: 04544- 246 500, 246501, 246502. Website : www.nprcolleges.org, www.nprcet.org, Email nprcetprincipal@nprcolleges.org</p>	
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ANNA UNIVERSITY ASSESSMENT ENTRY

DEPARTMENT OF COMPUTER SCIENCE ENGINEERING

The sample of internal marks entered in Anna University web portal is attached below.

- Period 1 – Entry of Attended hours only
- Period 2 – Entry of internal marks (**IM1**)
- Period 3 – Entry of internal marks (**IM2**)
- Period 4 – Entry of internal marks (**MODEL**)



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ANNA UNIVERSITY :: CHENNAI - 600 025 OFFICE OF THE CONTROLLER OF EXAMINATIONS

Assessment Details Entered

APRIL / MAY EXAMINATION, 2020 - EXAMINATIONS

Inst Code & Name : 9208 - N P R COLLEGE OF ENGINEERING AND TECHNOLOGY

Branch Code / Name : 104 : B.E. Computer Science and Engineering University : AUC
Semester : 08

Register No.	Name of the Student	Subjects	Attend hr 1	Total hr 1	Attend hr 2	Total hr2	IM 2	Attend hr 3	Tot hr 3	IM 3	Attend hr 4	Total hr4	IM 4	
920816104001	ABIRAMI M	CS6008	13	13	21	21	86	20	21	85	21	21	0	
		CS6801	13	13	18	18	84	20	22	89	20	21	0	
		CS6811										178	180	99
		GE6075	9	9	18	18	80	14	14	85	22	22	0	
920816104002	ARUNA DEVI P	CS6008	13	13	14	21	90	20	21	99	21	21	0	
		CS6801	12	13	11	18	84	19	22	99	21	21	0	
		CS6811										180	180	98
		GE6075	5	9	11	18	82	14	14	96	21	22	0	
920816104003	ASHWIN SARAVANAN RC	CS6008	5	13	12	21	24	16	21	60	21	21	0	
		CS6801	4	13	11	18	54	11	22	67	21	21	0	
		CS6811										178	180	91
		GE6075	5	9	12	18	18	7	14	43	22	22	0	
920816104004	BALAJI J	CS6008	6	13	18	21	50	19	21	76	21	21	0	
		CS6801	4	13	16	18	64	20	22	60	21	21	0	
		CS6811										179	180	94
		GE6075	4	9	15	18	66	14	14	75	22	22	0	
920816104005	BALAJI R M	CS6008	2	13	15	21	60	14	21	83	21	21	0	
		CS6801	2	13	9	18	56	13	22	40	21	21	0	
		CS6811										180	180	91
		GE6075	2	9	9	18	50	8	14	53	22	22	0	
920816104006	CHARUMATHY B	CS6008	8	13	10	21	68	15	21	76	21	21	0	
		CS6801	8	13	9	18	78	15	22	69	21	21	0	
		CS6811										180	180	97
		GE6075	6	9	9	18	66	7	14	75	22	22	0	
920816104008	DIVYA A	CS6008	12	13	18	21	82	20	21	92	21	21	0	
		CS6801	11	13	15	18	56	18	22	89	21	21	0	
		CS6811										180	180	99
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920816104009	DURGADEVI P	CS6008	10	13	15	21	92	20	21	89	20	21	0	
		CS6801	10	13	15	18	76	21	22	89	21	21	0	
		CS6811										180	180	98
		GE6075	7	9	13	18	84	14	14	95	21	22	0	
920816104010	GOMATHI AKILA D	CS6008	13	13	21	21	68	21	21	95	21	21	0	
		CS6801	13	13	18	18	86	20	22	96	21	21	0	
		CS6811										180	180	98
		GE6075	9	9	12	18	70	14	14	97	22	22	0	
920816104011	GURU PRASATH S	CS6008	6	13	18	21	46	18	21	80	20	21	0	
		CS6801	5	13	16	18	68	19	22	84	20	21	0	
		CS6811										180	180	95
		GE6075	6	9	14	18	60	10	14	88	22	22	0	
920816104012	HARI VIGNESH S	CS6008	5	13	13	21	62	18	21	59	21	21	0	
		CS6801	4	13	13	18	80	19	22	83	20	21	0	
		CS6811										180	180	99
		GE6075	5	9	14	18	74	11	14	61	22	22	0	
920816104013	ILAMATHI G	CS6008	8	13	21	21	90	15	21	88	21	21	0	
		CS6801	7	13	18	18	88	20	22	63	21	21	0	
		CS6811										180	180	99
		GE6075	5	9	18	18	80	11	14	93	21	22	0	
920816104014	KANNAN B	CS6008	5	13	15	21	20	17	21	53	20	21	0	
		CS6801	4	13	13	18	62	16	22	52	21	21	0	
		CS6811										180	180	93
		GE6075	5	9	12	18	50	12	14	48	22	22	0	
920816104015	KARTHICK RAJA T	CS6008	8	13	17	21	32	16	21	51	21	21	0	



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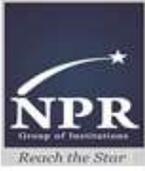


ANNA UNIVERSITY :: CHENNAI - 600 025 OFFICE OF THE CONTROLLER OF EXAMINATIONS

Assessment Details Entered
APRIL / MAY EXAMINATION, 2020 - EXAMINATIONS

Inst Code & Name : 9208 - N P R COLLEGE OF ENGINEERING AND TECHNOLOGY

	CS6801	7	13	17	18	26	19	22	39	21	21	0	
	CS6811									180	180	91	
	GE6075	6	9	17	18	34	11	14	35	22	22	0	
920816104016	KARTHIGA S	CS6008	8	13	21	21	62	17	21	77	20	21	0
	CS6801	8	13	18	18	58	20	22	53	21	21	0	
	CS6811									180	180	96	
	GE6075	7	9	18	18	80	14	14	89	22	22	0	
920816104017	KEERTHANA M	CS6008	9	13	19	21	84	21	21	99	21	21	0
	CS6801	8	13	16	18	78	20	22	95	20	21	0	
	CS6811									180	180	99	
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920816104018	MADHAN N	CS6008	3	13	15	21	52	15	21	57	21	21	0
	CS6801	3	13	12	18	66	14	22	56	21	21	0	
	CS6811									180	180	93	
	GE6075	2	9	12	18	60	9	14	41	22	22	0	
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	CS6811									178	180	98	
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	CS6811									180	180	95	
	GE6075	6	9	10	18	65	9	14	57	22	22	0	
920816104021	MOINUDEEN S	CS6008	2	13	15	21	0	17	21	84	20	21	0
	CS6801	2	13	14	18	64	21	22	77	21	21	0	
	CS6811									179	180	95	
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	CS6801	12	13	17	18	46	16	22	77	21	21	0	
	CS6811									180	180	98	
	GE6075	7	9	16	18	70	10	14	81	22	22	0	
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	CS6801	4	13	7	18	28	18	22	41	21	21	0	
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920816104025	PRASATH T	CS6008	9	13	18	21	28	18	21	77	21	21	0
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	CS6801	7	13	18	18	64	18	22	47	21	21	0	
	CS6811									180	180	98	



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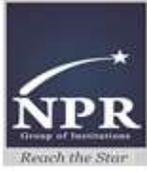


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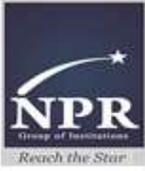


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	CS6811									178	180	99
	GE6075	8	9	12	18	90	14	14	97	22	22	0



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CONTINUOUS INTERNAL EVALUATION (CIE) REPORT TO PARENTS

ABSENT REPORT TO THE PARENTS

9:51 Signal 89%

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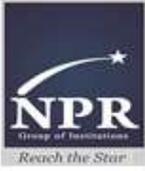
Dear Parent your
Son/Daughter
VISHNU GIRI
K III MECH
B Absent On
09.12.2019 2:02 pm

Friday, 20 December 2019

Dear Parent your
Son/Daughter
VISHNU GIRI
K III MECH
B Absent On
20.12.2019 12:02 pm

S.VISHNU GIRI-III-Mech-B Absent SMS





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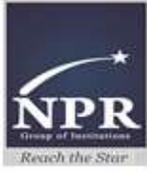
PROFILE		EXAM SCHEDULE		REG. PREVIEW		ASSESSMENT		EXAM RESULTS		ELECTIVE		GRIEVANCE	
Subject Code	Report Period I		Report Period II			Report Period III			Total Per				
	Total Attended Period	Period	Total Attended Period	Period	Assessment Mark(Out of 100)	Total Attended Period	Period	Assessment Mark(Out of 100)					
GE6757	21	20	16	14	89	18	16	97					
ME6005	22	21	26	25	56	19	17	100					
ME6012	15	14	23	19	44	22	21	95					
ME6701	18	17	19	17	47	21	18	56					
ME6702	25	24	21	18	82	24	23	89					
ME6703	24	23	19	17	44	13	12	79					

Note:

1. The attendance details are given only in terms of periods. If the details are in a format like % of attendance, you may approach the concerned faculty members for necessary changes.
2. The assessment marks obtained are displayed for the maximum of 100 marks.
3. If any discrepancy is noticed by a student, it may be clarified with the concerned members.

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ANNA UNIVERSITY RESULT



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Pre- Examination Monitoring System

Welcome AMRUTHAA SHREE R

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- EXAM SCHEDULE
- REGISTRATION
- ADMISSION
- RESULT
- FEEDBACK
- LINK PAGE

Exit

Result for April / May Examination,2020 [R-2017]

Register Number	920817104009			
Name	AMRUTHAA SHREE R			
Branch	B.E. Computer Science and Engineering			
Semester	Subject Code	Grade	Result	
06	CS8601	A+	PASS	
06	CS8602	O	PASS	
06	CS8603	A+	PASS	
06	CS8611	O	PASS	
06	CS8651	A+	PASS	
06	CS8661	O	PASS	
06	CS8662	O	PASS	
06	CS8691	A+	PASS	
06	HS8581	O	PASS	
06	IT8076	O	PASS	

Note: The below GPA is calculated based on the above result only

GPA for semester : 06 -- 9.52

Result for Nov. / Dec. Examination, 2019 [R-2017]

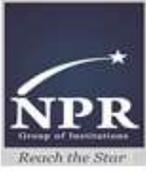
Register Number	920817104009			
Name	AMRUTHAA SHREE R			
Branch	B.E. Computer Science and Engineering			
Semester	Subject Code	Grade	Result	
05	CS8501	B+	PASS	
05	CS8581	O	PASS	
05	CS8582	O	PASS	
05	CS8591	A+	PASS	
05	CS8592	A+	PASS	
05	EC8681	O	PASS	
05	EC8691	B+	PASS	
05	MA8551	B+	PASS	
05	OMD551	A	PASS	

Note: The below GPA is calculated based on the above result only

GPA for semester : 05 -- 8.32

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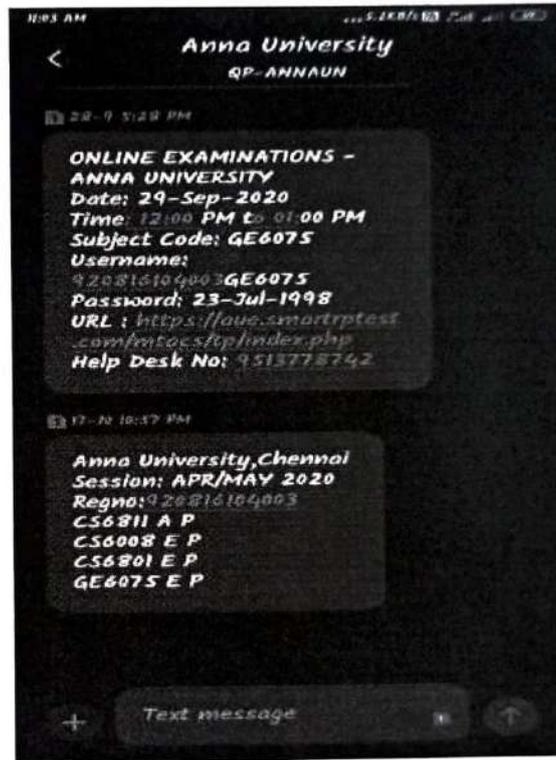


ANNA UNIVERSITY RESULT SENT TO THE PARENTS THROUGH SMS

TK ANNAUN

17/09/2020 Wed 11:14 AM

Anna University, Chennai
Session: NOV/DEC 2019
Regno: 920816104008
CS6007 D P
CS6701 C P
CS6702 E P
CS6703 C P
CS6704 D P
CS6711 S P
CS6712 S P
IT6801 C P





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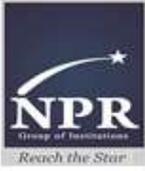
ERP STUDENTS LOGIN

The screenshot shows a web browser window at npr-cet.ac-school.com. The user is logged in as S. JANAKI. The dashboard includes a sidebar with navigation options: Home, Achievement, Fees, Hostel, Library, Examination, Documents, Profile, Log, and Complaints/Concerns. The main content area features a Notice Board with several webinar announcements, a Message section with 'No Messages', a Calendar for September 2020, and a News section with 'No News...'. The user's profile card shows a 'View Profile' button and 'Change Credentials' and 'Sign out' options.

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SAMPLE REPORTS OF (CIE)

1. Internal Examination Question
2. Answer key & sheet



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Internal Test Question Paper

Register Number:

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INTERNAL EXAMINATION I – AUGUST 2019

5th Semester – CSE

CS8591-Computer Networks

Duration: 90 Minutes

Date: 5/08/2019

Maximum marks: 50

Course Outcomes ,Question Number, Marks:

COs	CO1	CO2
Ques. No.	1,2,8	3,4,5,6,7
Max. Marks	20	30

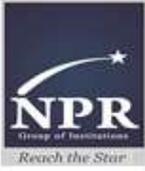
COs & K-Level:

CO1: Students can be able to **Identify** various layers of network and discuss the functions of physical layer

CO2: Students will be able to **Discuss** how data flows from one node to another node with regard to data link layer

K-Levels	K1: Remember	K2: Understand	K3:Apply	K4:Analyse	K5:Evaluate	K6:Create
----------	--------------	----------------	----------	------------	-------------	-----------

Q.No	Answer All Questions	Bloom's Taxonomy K-level
PART – A (5 x 2 = 10 marks)		
1.	Generalize LAN, WAN and MAN.	K1
2.	Define: Transmission media and its types.	K1
3.	Define the term Bandwidth and Latency.	K1
4.	Discuss the Unicast, Multicast, and Broadcast Addresses.	K2
5.	What is meant by Framing?	K2



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PART – B (1 x 8 = 8 marks)		
6.	Explain about network and its types.	K2
PART – C (2 x16 = 32 marks)		
7.	Explain in detail about switching and its types. 1.Circuit switching 2.Packet Switching	K6
8.	Explain in detail about the data link layer noisy protocols with neat diagram.	K6


FACULTY IN-CHARGE


HoD

Note: In Part C, questions can be set for 16 marks if the answer is very lengthy otherwise two or more sub-divisions can be included in each full question with break-up of marks indicated in brackets.





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Internal Test Answer Key

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

INTERNAL EXAMINATION I- AUGUST 2019

CS8591-COMPUTER NETWORKS

Answer Key

PART – A

1. Generalize LAN, WAN and MAN.

- It is privately owned and connects some hosts in a single office, buildings, or campus.
- It is a simply establishing network.
- Each host in LAN has an identifier, an address that uniquely defines the host in the LAN.
- WAN is the wide geographical span that connects even a country.

MAN :

- It is combination of both LAN and WAN.
- This network is provided to those who wanted to get the services of this particular network with in the cities

2. Define: Transmission media and its types.

- Transmission media are actually located below the physical layer are Directly controlled by the physical layer.
- We could say that the transmission media belong to physical layer.
- It can broadly define as anything that can carry information from source to destination.
- It is of two types they are
 1. Guided
 - A. Twisted pair cable
 - B. Coaxial cable
 - C. Fibre optic cable
 2. Unguided medium
 - Wireless
 - Radio waves
 2. micro waves.
 3. Infrared

3. Define the term Bandwidth and Latency.

Bandwidth: One of the characteristic that measures network performance is bandwidth. They are divided in to

1. Bandwidth in Hertz.

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2. Bandwidth in bits per second

Latency: It is defined how long it takes for an entire message to completely arrive at the destination from the time the first bit is sent from the source.

It is combination of four components.

4. Discuss the Unicast, Multicast, and Broadcast Addresses.

- A unicast address is an address that identifies a single node on a link as destination.
- It is a one-one communication.
- Some link-layer protocols define multicast addresses.
- It is a one-to-many communication method and data delivery scheme in which a single source sends the same data to a group of stations simultaneously in a link.

5. What is meant by Framing?

- The framing is the process in which the data link layer receives the packets from the network layer and divided them into smaller frames for transmission

PART-B

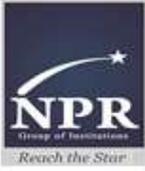
6. Explain about network and its types.

LAN : LOCAL AREA NETWORK –

- It is privately owned and connects some hosts in a single office, buildings, or campus.
- It is a simply establishing network.
- Each host in LAN has an identifier, an address that uniquely defines the host in the LAN.

WAN : WIDE AREA NETWORK –

- WAN is the wide geographical span that connects even a country.
- The interconnecting devices used such as switches, routers, or modems .
- It is owned by an organization.
- It is of two types
 1. Point to point WAN and
 2. Switched WAN.



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MAN : METROPOLITAN AREA NETWORK :

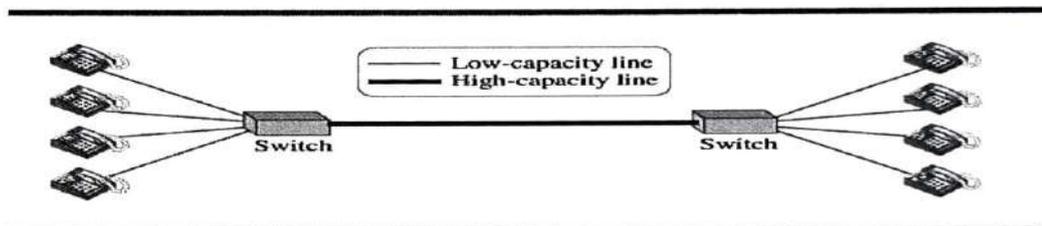
- It is combination of both LAN and WAN.
- This network is provided to those who wanted to get the services of this particular network with in the cities.

PART-C

7.Explain in detail about switching and its types.

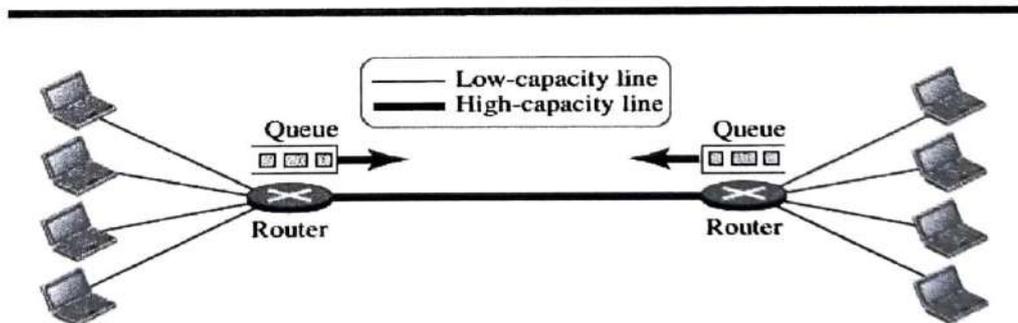
- Switches are able to create a temporary connection between two or more devices .
- It is of three types. They are

- circuit switched network



- Packet switched network - 1. Datagrams network

2.virtualized network



- Message switched network

8.Explain in detail about the data link layer noisy protocols with neat diagram.

Stop-and-Wait Automatic Repeat Request

- The Stop-and-Wait Automatic Repeat Request (Stop-and-Wait ARQ), adds a simple error control mechanism to the Stop-and-Wait Protocol.
- To detect and correct corrupted frames, we need to add redundancy bits to our data frame.
- When the frame arrives at the receiver site, it is checked and if it is corrupted, it is silently discarded.
- The detection of errors in this protocol is manifested by the silence of the receiver.

Go-Back-N Automatic Repeat Request

- In this protocol we can send several frames before receiving acknowledgments; we keep a copy of these frames until the acknowledgments arrive.

Selective Repeat Automatic Repeat Request

- Go-Back-N ARQ simplifies the process at the receiver site.
- The receiver keeps track of only one variable, and there is no need to buffer out-of-order frames; they are simply discarded.
- However, this protocol is very inefficient for a noisy link.
- In a noisy link a frame has a higher probability of damage, which means the resending of multiple frames.
- This resending uses up the bandwidth and slows down the transmission.





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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

ANSWER SHEET VERIFIED BY STUDENTS

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INTERNAL TEST - I / H

- a) Register Number : 9 2 0 8 1 7 1 0 4 0 0 9
- b) Year & Department : III R CSE
- c) Subject code & Title : CS8591 - COMPUTER NETWORKS
- d) Date & Session (FN/AN) : 05.08.2019 ← FN
- e) Number of pages used : 10


Name and Signature of the Hall Superintendent with Date

Students has to put a tick (✓) mark for the questions attended in tick mark column

PART-A			
Question No.	✓	CO...1. Marks	CO...2. Marks
1.	✓	2	
2.	✓	2	
3.	✓		2
4.	✓		2
5.	✓		2
TOTAL		4	6
TOTAL MARKS		10	

Students has to put a tick (✓) mark for the questions attended in tick mark column

PART-B & C							
Question No.	✓	i		(ii)		Total Marks	
		CO...1. Marks	CO...2. Marks	✓	CO...1. Marks		CO...2. Marks
6.	✓		6	-	-	6	
7.	a)	✓	14		-	-	14
	b)						
8.	a)	✓	14		-	-	14
	b)						
TOTAL			14	20	-	-	34

CO. No.	Total Marks	Attained Marks
CO...1.	20	18
CO...2.	30	26

Grand Total	44
	50

Verified
Amal


Name and Signature of the Faculty

[V. SUJITHA, AP/CSE]


Name and Signature of the HoD

[J. VISWANATH
HOD - CSE]

part : A :

1. LAN, MAN, WAN :

Local Area Network [LAN]	Metropolitan Area Network [MAN]	Wide Area Network WAN.
USE: Building office, School, house.	USE: Multiple building, or in a city.	USE: Multiple city, Country.
NETWORK RANGE: 10 m - 1000 m	NETWORK RANGE: 5 km - 50 km	NETWORK RANGE: 1000000 km.
EX: Ethernet, Wifi	EX: Ethernet, Wifi, [Cable TV network]	EX: Ethernet, Wifi, [Internet]

2. Transmission media: In data communication terminology, a transmission medium is a physical path between the transmitter and the receiver.

Types:

1. Guided media

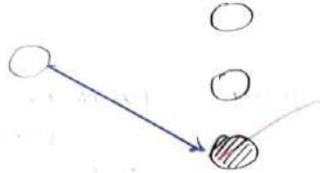


2. Unguided Media → space.

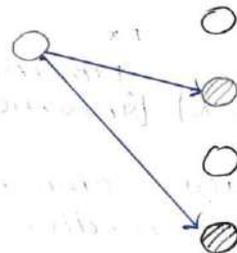
3. Latency: Latency is the amount of time it takes for data to travel from one point to another. It is dependant on the physical distance that data must travel through cords, networks, and the like to reach its destination.

Bandwidth: Is the rate of data transfer for a fixed period of time.

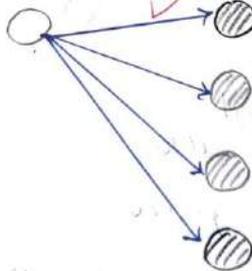
4. UNICAST ADDRESS: Has One sender and One receiver



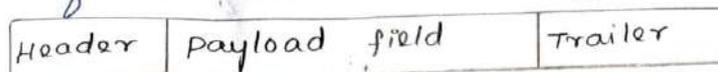
MULTICAST ADDRESS: One sender (potentially many senders), many receivers



BROADCAST ADDRESS: One sender, all the others as receivers.



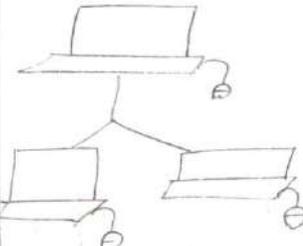
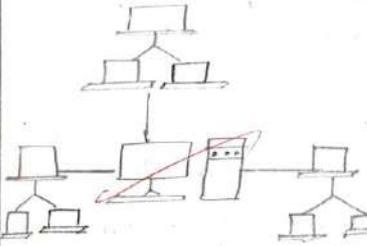
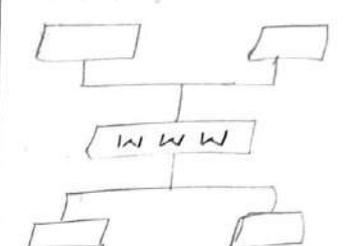
5. FRAMING: Framing is a point-to-point connection between two computers (or) devices consists of a wire in which data is transmitted as a stream of bits. However, these bits must be framed into discernible blocks of information. Framing is a function of the data link layer.



(2)

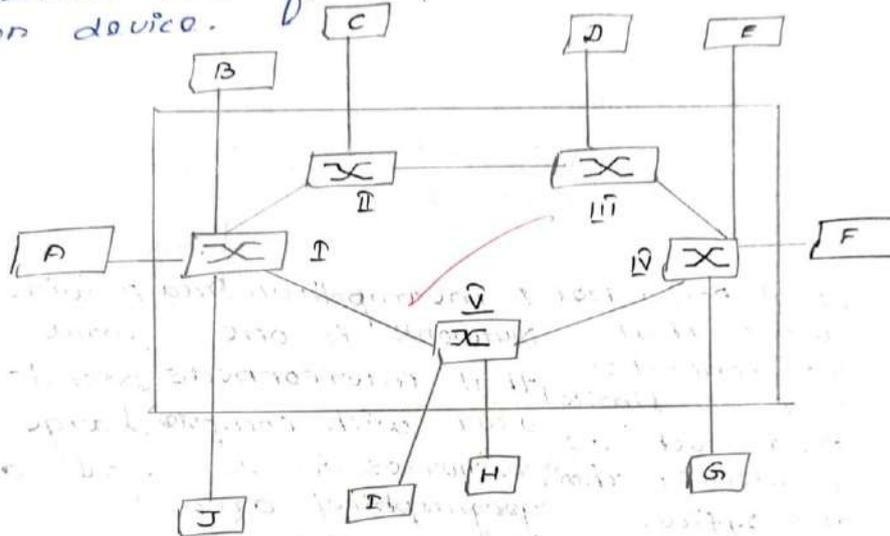
Part: B:

6. Network and its types: A network is one which is made up of connected devices and system

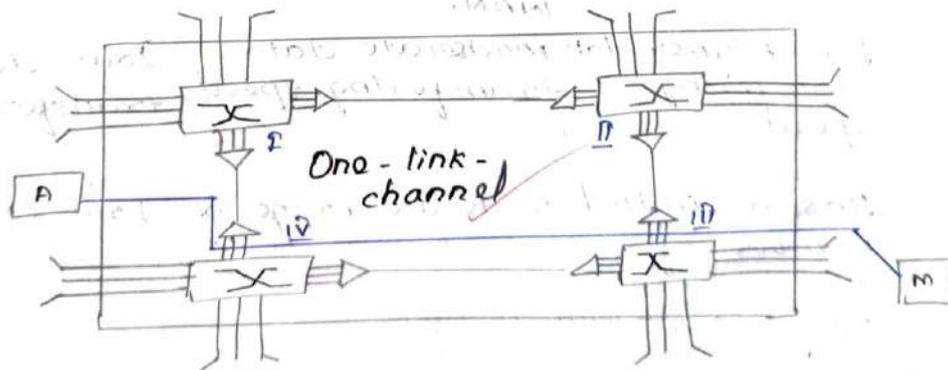
LAN	MAN	WAN
		
<p>Local Area Network that interconnects within a limited area such as residence, school, and office.</p>	<p>A metropolitan Area Network is one that interconnects user with computer resources in a geographical area larger than LAN but smaller than WAN.</p>	<p>A wide Area Network is one that extends over a large geographical area.</p>
<p>Has Higher data transferring speed</p>	<p>Moderate data transferring speed</p>	<p>Low data transferring speed</p>
<p>Has a limited number of user - less congestion</p>	<p>Moderate congestion</p>	<p>Low Congestion</p>
<p>Easier to design and maintain</p>	<p>Difficult and Complicated to design and maintain.</p>	<p>Difficult and complicated to design and maintain</p>
<p>Uses high bandwidth for data transfer</p>	<p>Has low bandwidth for data transfer.</p>	<p>Bandwidth is moderate for data transfer.</p>

part : c :

7. Switching: A network switch is networking hardware that connects devices on a computer network by using packet switching to receive and forward data to the destination device.



(i) circuit Switching:



* When (A) system has to communicate with end system (M), A has to send a request and if all the switch accepts then connection is established to M, then it is called as set up phase.

* It takes place in the physical layer. The resources must be reserved, then only communication takes place. Here, no addressing is involved.

* According to communication, it is divided into three phases:

1. Set-up phase
2. Data-transfer phase
3. Connection-tear down.

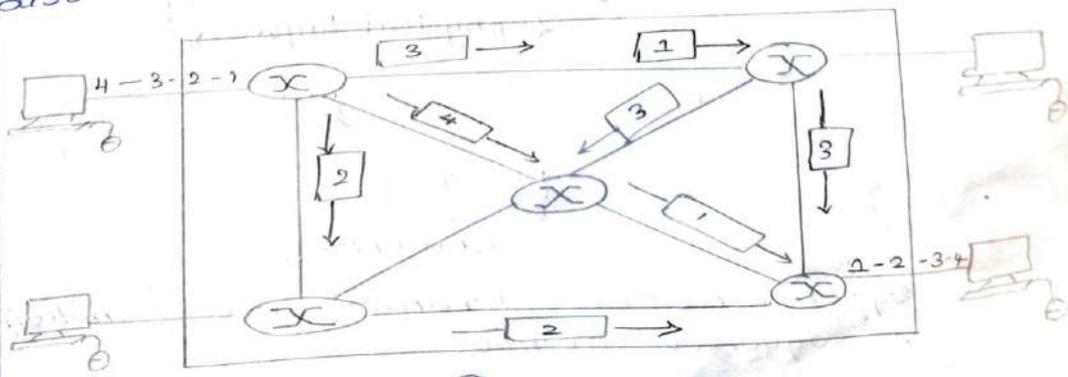
Data-transfer: Data-transferred from the request device to another when the request is accepted.

Tear-down-phase: Removing (or) cleaning the established connection.

* Efficiency and
* Delay } are measured here.

ii) packet-switched network: Here no resource reservation. The resource is allocated based on FCFS, where the reservation is done on demand.

* Data-gram networks: It is done in a network layer where packets of a message is also called as datagram.



* Routing table: To look after the connection (packets - sent - in - order) between the sender and the receiver, and to decide the destination address the routing table is used.

Efficiency: Delay will not be there

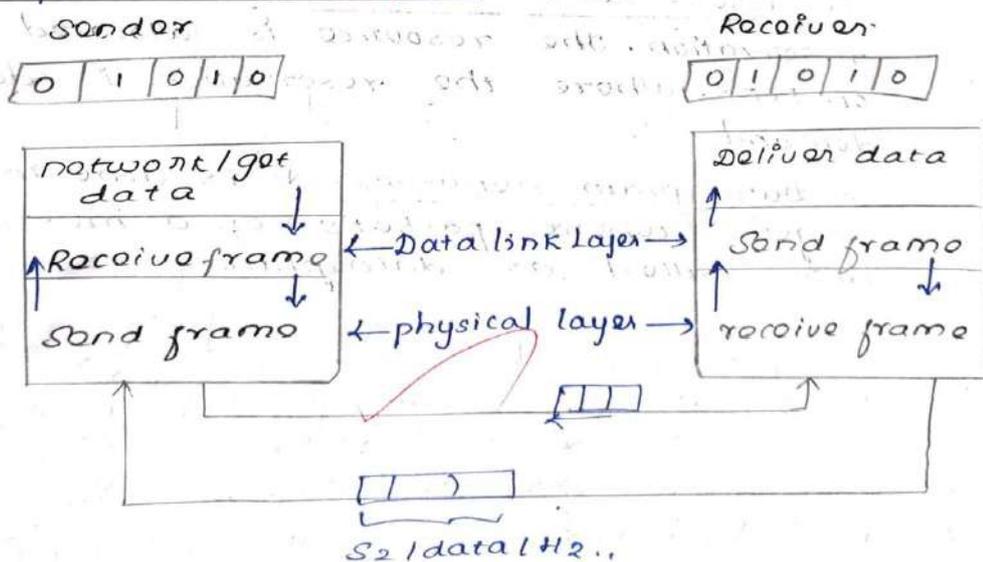
8. Data-link-Layer protocols: protocols is a set of rules that needs to be implemented in a software and run by 2 nodes and involved in data exchange at the data-link layer.

2 TYPES: NOISE-LESS CHANNEL - Error free
NOISY CHANNEL - Error creating.

NOISY CHANNEL:

- 1, Stop and Wait ARQ.
- 2, Goback NARQ.

Stop and Wait ARQ.



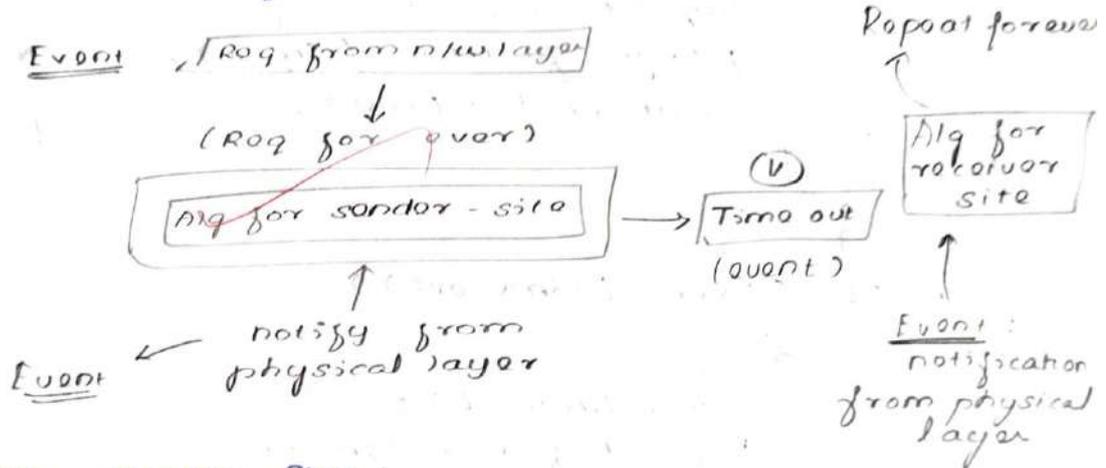
Sequence number: Frames are numbered using a sequence number

If the frame is a bit long, then frames goes to 0 to $2^m - 1$ (or) based to modulo 2 arithmetic [0, 1, 0, 1] are assigned to do frame.

* Acknowledge number: The acknowledge number for the next frame should be received.

* Data frame → sequence number

* ACK frame → ACK number

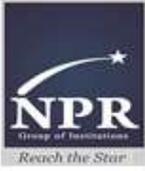


Alg: Sender Site:

```

Sn = 0;
can send = true;
while (true)
{
    wait for event ();
    if (Event (request to send) AND can send)
    {
        Get data ();
        Make frame (sn);
        Store frame (sn);
        Send frame (sn);
        Start timer ();
        sn = sn + 1;
    }
}

```



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```
can send = false;
wait_for_Event ();
if (Event (Arrival notification))
{
    Receive Frame (ack no);
    if (not corrupted AND ack no = sn)
    {
        stop timer ();
        purge frame (sn-1);
        cansend = true;
    }
}
if (Event (Time out))
{
    start timer ();
    Resend frame (sn-1);
}
}
```

Receiver - Site;

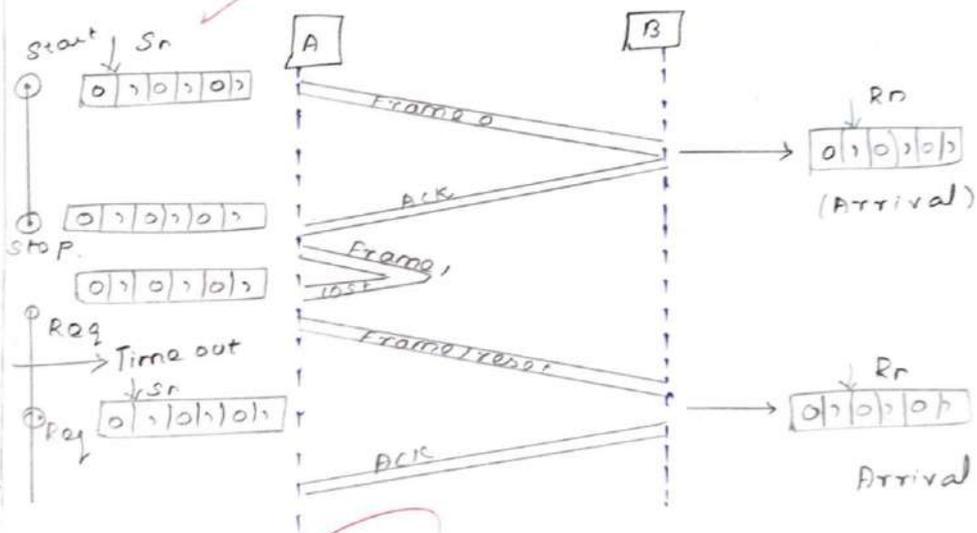
```
Rn = 0;
while (true)
{
    wait_for_Event ();
    if (Event (Arrival Not))
    {
        Receive Frame ();
        if (corrupted (frame))
            sleep ();
        if (seq no == Rn)
            (3)
```

```

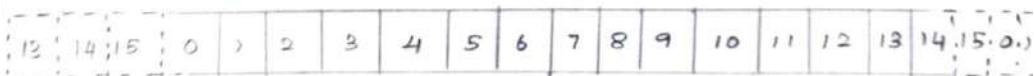
    {
      Extract data ();
      Deliver data ();
      Rn = Rn + 1;
    }
    Send Frame (Rn);
  }
}

```

Flow Diagram



Go-back NARQ

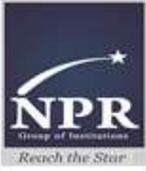


Frames already acknowledged

Frames sent but, no ack arrived.

Frames that can be sent, but not received by upper layer.

Frame that can't be sent.



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Go-back - N - automatic Repeat Request
(Go-back - N - ARQ), is a data-link layer
protocol that uses a sliding window me-
thod for reliable and sequential delivery
of data frames. It is a case of sliding
window protocol having to send window
size of N and receiving window size of:



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MODEL PRACTICAL EXAMINATION SCHEDULE

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

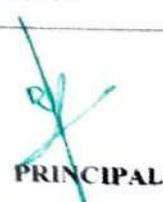
ACADEMIC YEAR(2019-2020) – ODD SEMESTER

MODEL PRACTICAL EXAMINATION – NOV/DEC2019

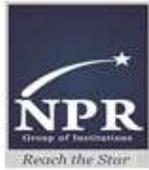
Year	Subject	Date & Session
II	CS8383-OBJECT ORIENTED PROGRAMMING LABORATORY	15.10.2019 FN & AN
II	CS8381-DATA STRUCTURES LABORATORY	16.10.2019 FN & AN
II	CE8382 - DIGITAL SYSTEMS LABORATORY	17.10.2019 FN & AN
II	HS8381- INTERPERSONAL SKILLS LABORATORY	18.10.2019 FN & AN
III	CS8581 – NETWORKS LABORATORY	16.10.2019 FN & AN
III	CS8582 - OBJECT ORIENTED ANALYSIS AND DESIGN LABORATORY	17.10.2019 FN & AN
III	EC8681 - MICROPROCESSORS AND MICROCONTROLLERS LABORATORY	18.10.2019 FN & AN
IV	CS6711 SECURITYLABORATORY	17.10.2019 FN & AN
IV	CS6712 GRID AND CLOUD COMPUTING LABORATORY	18.10.2019 FN & AN


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MODEL PRACTICAL EXAMINATION

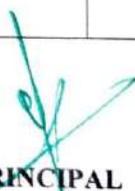
LABORATORY SCHEDULE –CS6711 SECURITYLABORATORY

VENUE: CP LAB

S.No	Degree/ Branch/ Semester	Date of Exam	Time	RegisterNo	Total No of Students
1	B.E / CSE/ VII SEM- BATCH-I	17.10.2019	9.30 AM- 12.30 PM	920816104001 – 920816104006, 920816104008 -920816104024	23
2	B.E / CSE/ VII SEM- BATCH- II	17.10.2019	1.00PM- 4.00PM	920816104025 – 920816104036, 920816104038 - 920816104044,920816104301, 920816104701, 920816104902	22

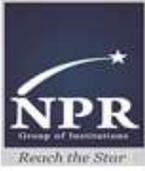

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MODEL PRACTICAL EXAMINATION

LABORATORY SCHEDULE

CS6712 GRID AND CLOUD COMPUTING LABORATORY

VENUE: OOAD LAB

S.No	Degree/ Branch/ Semester	Date of Exam	Time	RegisterNo	Total No of Students
1	B.E / CSE/ VII SEM- BATCH-I	18.10.2019	9.30 AM- 12.30 PM	920816104001 – 920816104006, 920816104008 -920816104024	23
2	B.E / CSE/ VII SEM- BATCH-II	18.10.2019	1.00PM- 4.00PM	920816104025 – 920816104036, 920816104038 - 920816104044,920816104301, 920816104701, 920816104902	22


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MODEL PRACTICAL EXAMINATION

LABORATORY SCHEDULE

CS8582 - OBJECT ORIENTED ANALYSIS AND DESIGN LABORATORY

VENUE: OOAD LAB

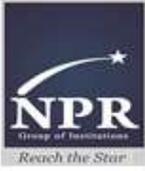
S.No	Degree/ Branch/ Semester	Date of Exam	Time	RegisterNo	Total No of Students
1	B.E / CSE/ V SEM- BATCH-I	17.10.2019	9.30 AM- 12.30 PM	920817104001-920817104013, 920817104015, 920817104017- 920817104028	26
2	B.E / CSE/ V SEM- BATCH-II	17.10.2019	1.00PM- 4.00PM	920817104029-920817104030, 920817104032, 920817104034- 920817104049, 920817104051- 920817104056	25


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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

MODEL PRACTICAL EXAMINATION

LABORATORY SCHEDULE CS8581 – NETWORKS LABORATORY

VENUE:CSE MAIN LAB

S.No	Degree/ Branch/ Semester	Date of Exam	Time	RegisterNo	Total No of Students
1	B.E / CSE/ V SEM- BATCH-I	16.10.2019	9.30 AM- 12.30 PM	920817104001-920817104013, 920817104015, 920817104017- 920817104028	26
2	B.E / CSE/ V SEM- BATCH-II	16.10.2019	1.00PM- 4.00PM	920817104029-920817104030, 920817104032, 920817104034- 920817104049, 920817104051- 920817104056	25

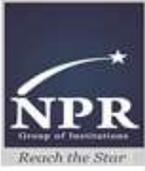

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MODEL PRACTICAL EXAMINATION

LABORATORY SCHEDULE

CS8383-OBJECT ORIENTED PROGRAMMING LABORATORY

VENUE:CSE MAIN LAB

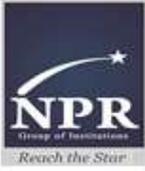
S.No	Degree/ Branch/ Semester	Date of Exam	Time	RegisterNo	Total No of Students
1	B.E / CSE/ III SEM- BATCH-I	15.10.2019	9.30 AM- 12.30 PM	920818104002-920818104005, 920818104007, 920818104009- 920818104012, 920818104017, 920818104019-920818104023	15
2	B.E / CSE/ III SEM- BATCH-II	15.10.2019	1.00PM- 4.00PM	920818104024, 920818104026- 920818104031, 920818104033- 920818104040	15


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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

MODEL PRACTICAL EXAMINATION

LABORATORY SCHEDULE

CS8381-DATA STRUCTURES LABORATORY

VENUE: OOAD LAB

S.No	Degree/ Branch/ Semester	Date of Exam	Time	RegisterNo	Total No of Students
1	B.E / CSE/ III SEM- BATCH-I	16.10.2019	9.30 AM- 12.30 PM	920818104002-920818104005, 920818104007, 920818104009- 920818104012, 920818104017, 920818104019-920818104023	15
2	B.E / CSE/ III SEM- BATCH-II	16.10.2019	1.00PM- 4.00PM	920818104024, 920818104026- 920818104031, 920818104033- 920818104040	15

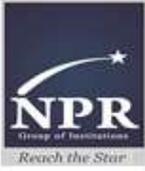

HOD/CSE




PRINCIPAL

Principal

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MODEL PRACTICAL EXAMINATION QUESTION

QP.
CODE

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B.E / B.Tech. LABORATORY EXAMINATIONS, OCTOBER / NOVEMBER 2020

Third Semester

Computer Science and Engineering

CS8381 - DATA STRUCTURES LABORATORY

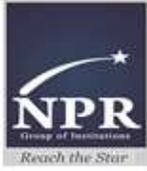
**(Common to Information Technology and Computer and Communication Engineering)
(Regulations - 2017)**

Time : 3 Hours

Max. Marks 100

AIM (10)	ALGORITHM (10)	PROGRAM (40)	OUTPUT (25)	VIVA VOCE (10)	RESULT (5)	TOTAL (100)

1.	Execute the push and pop operations performed on a stack of size 5 using C program.
2.	Implement First In First Out order of queue ADT using arrays in C.
3.	Write a C program to implement a sequential data structure - list ADT using array.
4.	Write a C program to implement stack ADT using linked list implementation
5.	Write a C program to implement queue ADT using linked list implementation
6.	Insert elements in a node with pointers and delete few elements using list ADT in C program.
7.	How will you apply stack in converting Infix to postfix using C? Write a program to implement the same.
8.	Write a C program to implement insertion and deletion on both sides of a list / application of queue ADT – DEQueue
9.	Extract the coefficient of a given exponent and write a program to perform addition using lists in C.
10.	Write a C program to implement an application of list ADT - Polynomial subtraction.
11.	Explain programmatically using C, how to perform the different traversals in a binary tree.
12.	How to implement the search operation on a binary search tree, after performing insertion operation using C.
13.	Implement a C program with the criteria, every descendant node's value in the left subtree of n is less than the value of n and every descendant node's value in the right subtree is greater than the



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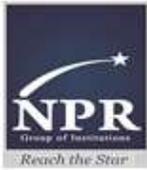
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	value n.
14.	Write a C program to implement a self balancing tree with nodal height difference of 1 unit.
15.	To insert 1 to 10 numbers in a tree with a balancing factor of 1 maximum, frame a code using C.
16.	Implement MIN Heap property using C.
17.	Substantiate the importance of MAX Heap in insertion and deletion by programming using C
18.	A person wants to visit some places. He starts from a vertex and then wants to visit every place connected to this vertex and so on. What traversal methodology suits best for him and implement it using C? (Breadth First Search).
19.	A person wants to visit some places. He starts from a vertex and then wants to visit every vertex till it finishes from one vertex, backtracks and then explore other vertex from same vertex. What traversal methodology suits best for him and implement it using C? (Depth First Search)
20.	Describe programmatically using C, how to find the shortest path as an application of graphs.
21.	How to locate an element in an array using a) linear search (5+20+10) b) binary search (10+20+15)
22.	Write a simple C program to narrate the execution of sorting using insertion sort / selection sort / bubble sort / shell sort
23.	Try coding the hashing function using linear probing in C language.


INTERNAL EXAMINER




HoD/CSE



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PRACTICAL RECORD NOTE

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Reach the Stars

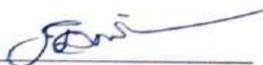
Name : R. Nivedha

Year : I Semester II Branch CSE

University Register No. 920818104020

CERTIFIED that this a Bonafide Record work done by the above student in the

CS8381 Data Structures Laboratory during the year 2019 - 2020

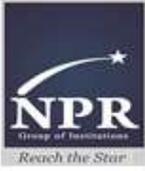

Signature of Lab. Incharge
(J. DINESH)


Signature of Head of the Dept.
[J. VISWANATH]

Date .. 02 .. 11 .. 19


Internal Examiner
(J. DINESH)


External Examiner



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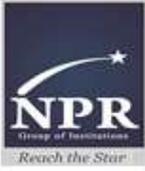
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CONTENTS

Sl. No.	Date Experiment	Name of the Experiment	Page No.	Date of Submission	Remarks
01.	08.07.19	Implementation of List ADT using Array	2	15.07.19	Pass
02.	08.07.19	Implementation of Stacking using Array	10	15.07.19	Pass
03.	15.07.19	Implementation of Queue using linked list	18	21.07.19	Pass
04.	15.07.19	Implementation of Polynomial Addition using linked list	26	21.07.19	Pass
05.	27.07.19	Implementation of Binary to Decimal Conversion	34	14.08.19	Pass
06.	27.07.19	Implementation of Binary Trees and operations	42	17.08.19	Pass
07.	17.08.19	Implementation of Binary tree	48	14.09.19	Pass
08.	17.08.19	Implementation of AVL tree	54	14.09.19	Pass
09.	14.09.19	Implementation of Heaps using Priority Queue.	62	20.09.19	Pass



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DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING MODEL PRACTICAL EXAMINATION TEST PAPER

Extract the coefficient of a given exponent and write a program to perform addition using lists in C.

J.P. Din
INTERNAL EXAMINER

REGISTER NUMBER : 920818104023

YEAR : II

SEMESTER : III

DATE : 16.10.2019.

SUB CODE & NAME : CS8381 - DATA STRUCTURES LABORATORY

COLLEGE CODE & NAME : 9208 - NPR COLLEGE OF ENGINEERING & TECHNOLOGY

AIM	ALGORITHM	PROGRAM	OUTPUT	VIVA	RESULT.
10	10	40	21	7	5

1. Extract the coefficient of a given exponent and write a program to perform addition using lists in C.

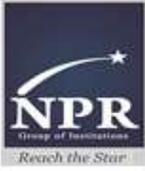
Aim:

To extract the coefficient of a given Exponent and to write a program to perform addition using lists in C.

Algorithm:

step 1 : Let P and Q be the two polynomial represented by linked list.

step 2 : while P and Q are not null, repeat step 3.



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step 3 : If the powers of two terms are equal then if the terms do not cancel then insert the sum of terms in to the sum polynomial.

Advance p ; Advance q .

step 4 : Else if the power of the first polynomial is greater than power of second polynomial then insert the term from the first polynomial into sum polynomial.

(Power of First polynomial > power of Second polynomial)
Advance p.

step 5 : else insert the term from second polynomial in to sum polynomial.

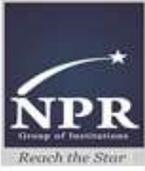
Advance q .

step 6 : copy the remaining terms from the non empty polynomial into the sum polynomial.

step 7 : The sixth step of algorithm is to processed till the end of polynomial has not been reached.

Program :

```
#include <stdio.h>
#include <math.h>
#include <conio.h>
struct link
{
    int coeff ;
    int pow ;
    struct link *next ;
};
```



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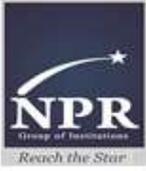
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```
struct link * Poly1 = NULL, * Poly2 = NULL, * Poly = NULL;
void create (struct link * Node)
{
    char ch;
    do
    {
        printf ("In enter coefficient : ");
        scanf ("%d", & node -> coeff);
        printf ("In enter power : ");
        scanf ("%d", & Node -> pow);
        node -> next = (struct link *) malloc (size of (struct
            link));
        node = node -> next;
        node -> next = NULL;
        printf ("In continue (Y/n) : ");
        ch = getch ();
    }
    while (ch == 'y' || ch == 'Y');
}
void show (struct link * node)
{
    while (node -> next != NULL)
    {
        printf ("%d x ^ %d", node -> coeff, node -> pow);
        node = node -> next;
        if (node -> next != NULL)
            printf (" + ");
    }
}
```



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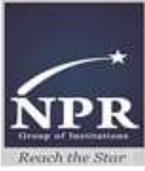
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```
void polyadd (struct link * poly 1, struct link * poly 2,
              struct link * poly)
{
    while (poly 1 -> next && poly 2 -> next)
    {
        if (poly 1 -> pow > poly 2 -> pow)
        {
            poly -> next = (struct link *) malloc (size of
                                                    (struct link));
            poly = poly -> next;
            poly -> next = NULL;
        }
        while (poly 1 -> next || poly 2 -> next)
        {
            if (poly 1 -> next)
            {
                poly -> pow = poly 1 -> pow;
                poly -> coeff = poly 1 -> coeff;
                poly 1 = poly 1 -> next;
            }
            if (poly 2 -> next)
            {
                poly -> pow = poly 2 -> pow;
                poly -> coeff = poly 2 -> coeff;
                poly 2 = poly 2 -> next;
            }
        }
    }
}
```



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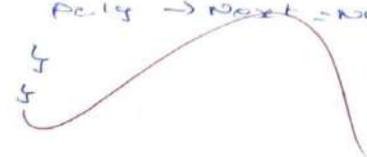
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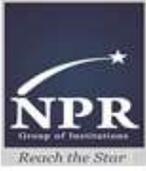
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```
poly → pow = poly 1 → pow ;  
poly → coeffd = poly 2 → coeffd ;  
poly 1 = poly 1 → next ;  
}  
else  
if (poly 1 → pow < poly 2 → pow)  
{  
poly → pow = poly 2 → pow ;  
poly → coeffd = poly 2 → coeffd ;  
poly 2 = poly 2 → next ;  
}  
else  
{  
poly → pow = poly 1 → pow ;  
poly → coeffd = poly 1 → coeffd + poly 2 → coeffd ;  
poly 1 = poly 1 → next ;  
poly 2 = poly 2 → next ;  
}  
poly → next = (struct link * ) malloc (size of  
struct link );  
poly = poly → next ;  
poly → next = NULL ;  
}
```





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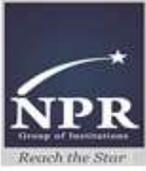
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```
void main ()
{
    char ch ;
    do
    {
        Poly 1 = (struct link *) malloc (size of (struct link));
        Poly 2 = (struct link *) malloc (size of (struct link));
        Poly = (struct link *) malloc (size of (struct link));
        Print f ("Enter 1st number : ");
        create (Poly 1);
        Print f ("Enter 2nd number : ");
        create (Poly 2);

        Print f ("1st number : ");
        show (Poly 1);
        Print f ("2nd number : ");
        show (Poly 2);
        Poly add (Poly 1, Poly 2, Poly);
        Print f ("Added polynomial : ");
        show (Poly);

        Print f ("Exit") ;
        ch = getch ();
    } while (ch == 'y' || ch == 'Y');
}
```



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output:

Enter 1st number : $5 \times x^3$
Enter coefficient : 5
Enter power : 3
continue (y/n) : n
Enter second number : $6 \times x^4 + 7 \times x^5$
Enter coefficient : 6
Enter power : 4
continue (y/n) : y
Enter coefficient : 7
Enter power : 5
continue (y/n) : n
1st number : $5 \times x^3 +$
2nd number : $6 \times x^4 + 7 \times x^5$
added polynomial : $5 \times x^3 + 6 \times x^4 + 7 \times x^5$

OP
verified

Result
JRM
10/10/2019

Thus the program to perform polynomial addition using linked list is executed & verified.



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PROJECT SCHEDULE

College code and Name: 9208-NPR College of Engineering and Technology

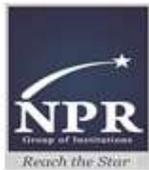
Year/Semester/Section: IV / 08

Branch : Computer Science and Engineering

Subject Code and Name: CS6811 – Project Work

Academic year : 2019- 2020 (Even)

BATCH NO	REGISTER NO	STUDENT NAME	PROJECT TITLE	PROJECT GUIDE
1	920816104001	ABIRAMILM	UNIVERSAL WRITING ROBOT EMPLOYMENT TO MIMIC HUMAN WRITING FOR MEDICAL APPLICATIONS	Mrs. V. SUJITHA, AP/CSE
	920816104013	ILAMATHI.G		
	920816104016	KARTHIGA.S		
2	920816104010	GOMATHI AKILA.D	AN OPTIMIZATION FRAMEWORK FOR HEART DISEASE PREDICTION IN IMBALANCED HIGH-DIMENSIONAL ICU BIG DATA	Mr. D. NAGARAJ, AP/CSE
	920816104031	SELVANAYAKI.C		
	920816104034	SHRI JANANIS		
	920816104038	SRINITHI.P		
3	920816104003	ASHWINSARAVANAN.R .C	CLASS ATTENDANCE SYSTEM	Mrs. V. SUJITHA, AP/CSE
	920816104030	SANTHOSHKUMAR.A		
	920816104033	SHREEDHAR.V		
	920816104036	SIVASUBRAMANIAN.C		
4	920816104004	BALAJIJ	WOMEN'S SAFETY BASED ON IOT	Mrs. J. BAMA, AP/CSE
	920816104018	MADHAN.N		
	920816104043	TAMIL SELVAN.M		
	920816104044	YOGESH JOHN.K		



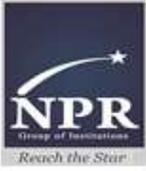
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5	920816104005	BALAJI.R.M	HANDGESTURE RECOGNITION APPLICATION FOR PHYSICAL DISABLED PEOPLE	Mrs. J. PRISCA MARY, AP/CSE
	920816104040	SUNDARAM.S.K.K		
	920816104041	SURYA.S		
	920816104701	KISHOREKUMAR D		
6	920816104006	CHARUMATHY.B	LANDSLIDE MONITORING SYSTEM	Mr. J. DINESH, AP/CSE
	920816104023	NANDHINI.S		
	920816104028	PREETHA.T		
7	920816104002	ARUNA DEVI.P	VIDEO BASED DYNAMIC HUMAN AUTHENTICATION SYSTEM FOR ACCESS CONTROL	Mrs. V. SUJITHA, AP/CSE
	920816104008	DIVYA.A		
	920816104009	DURGADEVI.P		
	920816104035	SHRUTHI.M		
8	920816104011	GURU PRASATH.S	IMAGE RECONSTRUCTION	Mrs. P.KALEESWARI, AP/CSE
	920816104025	PRASATH.T		
	920816104026	PRAVINVINO.M		
	920816104032	SENTHIL.N.P		
9	920816104012	HARIVIGESH.S	EARLY PREDICTION OF SEPSIS FROM CLINICAL DATA	Mrs. C. KALPANA, AP/CSE
	920816104022	MUTHU.T		
	920816104029	SABARIKRISHNANS.K		
	920816104301	S.SHARUKKHAN		
10	920816104014	KANNAN.B	PREDICTION OF ALZHEMIER'S DISEASE	Mrs. J. PRISCA MARY, AP/CSE
	920816104015	KARTHICKRAJA.T		
	920816104021	MOINUDEEN.S		
	920816104024	PAVITHRAN.M		



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11	920816104017	KEERTHANA.M	ELDERLY CARE SYSTEM USING IOT	Mrs. C. KALPANA, AP/CSE
	920816104020	MAHALAKSHMI.M		
	920816104901	NAVRIN BHAJELA		
12	920816104019	MADHUMITHA.A	REAL TIME BASED USING FACE RECOGNITION SYSTEM	Mr.D. NAGARAJ, AP/CSE
	920816104027	PREETHA.B		
	920816104039	SRIPRIYA.P		

Project coordinator



HoD-CSE



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PROJECT REVIEW & MARKS

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

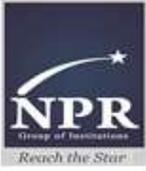
YEAR/ SEM: IV/08

SUB.CODE & NAME: CS6811 & PROJECT WORK

REVIEW : 00

DATE: 02.01.2020

S.NO	REGISTER NUMBER	NAME	PPT (10)	PRESENTATION (10)	ATTENDANCE (5)	TOTAL (25)
1	920816104001	ABIRAMI M	10	10	5	25
2	920816104002	ARUNA DEVI P	10	10	5	25
3	920816104003	ASHWIN SARAVANAN RC	10	9	4	23
4	920816104004	BALAJI J	10	9	4	23
5	920816104005	BALAJI R M	10	8	4	22
6	920816104006	CHARUMATHY B	10	10	5	25
7	920816104008	DIVYA A	10	10	5	25
8	920816104009	DURGADEVI P	10	9	5	24
9	920816104010	GOMATHI AKILA D	10	9	5	24
10	920816104011	GURU PRASATH S	10	8	5	23
11	920816104012	HARI VIGNESH S	10	10	5	25
12	920816104013	ILAMATHI G	10	9	5	24
13	920816104014	KANNAN B	10	8	5	23
14	920816104015	KARTHICK RAJA T	10	9	4	23
15	920816104016	KARTHIGA S	10	9	5	24
16	920816104017	KEERTHANA M	10	10	5	25
17	920816104018	MADHAN N	10	7	5	22
18	920816104019	MADHUMITHA A	10	10	5	25
19	920816104020	MAHALAKSHMI M	10	9	4	23
20	920816104021	MOINUDEEN S	10	9	4	23
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22	920816104023	NANDHINI S	10	10	5	25
23	920816104024	PAVITHRAN M	10	8	5	23
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25	920816104026	PRAVIN VINO M	10	9	5	24
26	920816104027	PREETHA B	10	10	5	25
27	920816104028	PREETHA T	10	10	5	25
28	920816104029	SABARI KRISHNAN S K	10	10	5	25
29	920816104030	SANTHOSH KUMAR A	10	8	5	23
30	920816104031	SELVANAYAKI C	10	10	5	25
31	920816104032	SENTHIL N P	10	9	5	24



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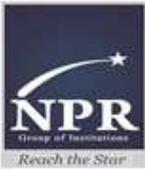
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32	920816104033	SHREEDHAR V	10	9	5	24
33	920816104034	SHRI JANANI S	10	10	5	25
34	920816104035	SHRUTHI M	10	10	4	24
35	920816104036	SIVASUBRAMANIAN C	10	9	5	24
36	920816104038	SRINITHI P	10	10	5	25
37	920816104039	SRIPRIYA P	10	10	5	25
38	920816104040	SUNDARAM S K K	10	9	5	24
39	920816104041	SURYA S	10	9	5	24
40	920816104043	TAMILSELVAN M	10	10	4	24
41	920816104044	YOGESH JOHN	10	9	5	24
42	920816104301	SHARUKKHAN S	10	10	4	24
43	920816104701	KISHORE KUMAR D	10	9	5	24
44	920816104901	NAVRIN BHAJILA A	10	10	4	24

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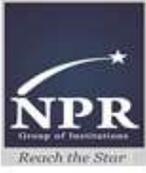
YEAR/ SEM: IV/08

SUB.CODE & NAME: CS6811 & PROJECT WORK

REVIEW : 01

DATE: 04.01.2020

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1	920816104001	ABIRAMI M	10	10	5	25
2	920816104002	ARUNA DEVI P	10	9	5	24
3	920816104003	ASHWIN SARAVANAN RC	10	7	5	22
4	920816104004	BALAJI J	10	9	5	24
5	920816104005	BALAJI R M	10	8	5	23
6	920816104006	CHARUMATHY B	10	10	4	24
7	920816104008	DIVYA A	10	9	5	24
8	920816104009	DURGADEVI P	10	10	5	25
9	920816104010	GOMATHI AKILA D	10	10	5	25
10	920816104011	GURU PRASATH S	10	10	4	24
11	920816104012	HARI VIGNESH S	10	10	5	25
12	920816104013	ILAMATHI G	10	10	5	25
13	920816104014	KANNAN B	10	9	5	24
14	920816104015	KARTHICK RAJA T	10	7	5	22
15	920816104016	KARTHIGA S	10	9	5	24
16	920816104017	KEERTHANA M	10	10	4	24
17	920816104018	MADHAN N	10	8	5	23
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19	920816104020	MAHALAKSHMI M	10	9	4	23
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22	920816104023	NANDHINI S	10	10	5	25
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25	920816104026	PRAVIN VINO M	10	10	5	25
26	920816104027	PREETHA B	10	9	4	23
27	920816104028	PREETHA T	10	10	5	25
28	920816104029	SABARI KRISHNAN S K	10	9	5	24
29	920816104030	SANTHOSH KUMAR A	10	9	5	24
30	920816104031	SELVANAYAKI C	10	10	5	25
31	920816104032	SENTHIL N P	10	10	5	25



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32	920816104033	SHREEDHAR V	10	10	5	25
33	920816104034	SHRI JANANI S	10	9	5	24
34	920816104035	SHRUTHI M	10	10	5	25
35	920816104036	SIVASUBRAMANIAN C	10	9	5	24
36	920816104038	SRINITHI P	10	9	5	24
37	920816104039	SRIPRIYA P	10	10	5	25
38	920816104040	SUNDARAM S K K	10	9	5	24
39	920816104041	SURYA S	10	9	5	24
40	920816104043	TAMILSELVAN M	10	10	5	25
41	920816104044	YOGESH JOHN	10	9	5	24
42	920816104301	SHARUKKHAN S	10	10	5	25
43	920816104701	KISHORE KUMAR D	10	7	5	22
44	920816104901	NAVRIN BHAIJILA A	10	10	5	25

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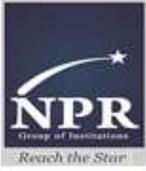
YEAR/ SEM: IV/08

SUB.CODE & NAME: CS6811 & PROJECT WORK

REVIEW : 02

DATE: 29.01.2020

S.NO	REGISTER NUMBER	NAME	PPT (10)	PRESENTATION (10)	ATTENDANCE (5)	TOTAL (25)
1	920816104001	ABIRAMI M	10	10	4	24
2	920816104002	ARUNA DEVI P	10	9	5	24
3	920816104003	ASHWIN SARAVANAN RC	10	8	5	23
4	920816104004	BALAJI J	10	9	5	24
5	920816104005	BALAJI R M	10	7	5	22
6	920816104006	CHARUMATHY B	10	10	4	24
7	920816104008	DIVYA A	10	10	5	25
8	920816104009	DURGADEVI P	10	9	5	24
9	920816104010	GOMATHI AKILA D	10	9	5	24
10	920816104011	GURU PRASATH S	10	8	5	23
11	920816104012	HARI VIGNESH S	10	10	4	24
12	920816104013	ILAMATHI G	10	10	5	25
13	920816104014	KANNAN B	10	8	5	23
14	920816104015	KARTHICK RAJA T	10	8	5	23
15	920816104016	KARTHIGA S	10	9	5	24
16	920816104017	KEERTHANA M	10	10	5	25
17	920816104018	MADHAN N	10	9	5	24
18	920816104019	MADHUMITHA A	10	10	4	24
19	920816104020	MAHALAKSHMI M	10	10	4	24
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23	920816104024	PAVITHRAN M	10	8	5	23
24	920816104025	PRASATH T	10	9	5	24
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26	920816104027	PREETHA B	10	9	5	24
27	920816104028	PREETHA T	10	10	5	25
28	920816104029	SABARI KRISHNAN S K	10	9	5	24
29	920816104030	SANTHOSH KUMAR A	10	8	5	23
30	920816104031	SELVANAYAKI C	10	10	5	25
31	920816104032	SENTHIL N P	10	9	5	24



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32	920816104033	SHREEDHAR V	10	10	5	25
33	920816104034	SHRI JANANI S	10	9	5	24
34	920816104035	SHRUTHI M	10	10	4	24
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36	920816104038	SRINITHI P	10	9	5	24
37	920816104039	SRIPRIYA P	10	10	5	25
38	920816104040	SUNDARAM S K K	10	9	5	24
39	920816104041	SURYA S	10	9	5	24
40	920816104043	TAMILSELVAN M	10	10	4	24
41	920816104044	YOGESH JOHN	10	9	5	24
42	920816104301	SHARUKKHAN S	10	10	5	25
43	920816104701	KISHORE KUMAR D	10	7	5	22
44	920816104901	NAVRIN BHAIJILA A	10	10	5	25

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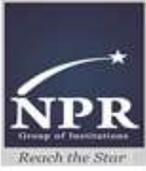
YEAR/ SEM: IV/08

SUB.CODE & NAME: CS6811 & PROJECT WORK

REVIEW : 03

DATE: 10.02.2020

S.NO	REGISTER NUMBER	NAME	PPT (10)	PRESENTATION (10)	ATTENDANCE (5)	TOTAL (25)
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2	920816104002	ARUNA DEVI P	10	10	5	25
3	920816104003	ASHWIN SARAVANAN RC	10	8	5	23
4	920816104004	BALAJI J	10	8	5	23
5	920816104005	BALAJI R M	10	9	5	24
6	920816104006	CHARUMATHY B	10	9	5	24
7	920816104008	DIVYA A	10	10	5	25
8	920816104009	DURGADEVI P	10	10	5	25
9	920816104010	GOMATHI AKILA D	10	10	5	25
10	920816104011	GURU PRASATH S	10	10	5	25
11	920816104012	HARI VIGNESH S	10	10	5	25
12	920816104013	ILAMATHI G	10	10	5	25
13	920816104014	KANNAN B	10	8	5	23
14	920816104015	KARTHICK RAJA T	10	8	5	23
15	920816104016	KARTHIGA S	10	9	5	24
16	920816104017	KEERTHANA M	10	10	5	25
17	920816104018	MADHAN N	10	9	5	24
18	920816104019	MADHUMITHA A	10	10	5	25
19	920816104020	MAHALAKSHMI M	10	10	5	25
20	920816104021	MOINUDEEN S	10	10	5	25
21	920816104022	MUTHU T	10	9	5	24
22	920816104023	NANDHINI S	10	10	5	25
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24	920816104025	PRASATH T	10	9	5	24
25	920816104026	PRAVIN VINO M	10	8	5	23
26	920816104027	PREETHA B	10	10	5	25
27	920816104028	PREETHA T	10	9	5	24
28	920816104029	SABARI KRISHNAN S K	10	10	5	25
29	920816104030	SANTHOSH KUMAR A	10	10	5	25
30	920816104031	SELVANAYAKI C	10	9	5	24
31	920816104032	SENTHIL N P	10	10	5	25



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32	920816104033	SHREEDHAR V	10	10	5	25
33	920816104034	SHRI JANANI S	10	10	5	25
34	920816104035	SHRUTHI M	10	10	5	25
35	920816104036	SIVASUBRAMANIAN C	10	9	5	24
36	920816104038	SRINITHI P	10	10	5	25
37	920816104039	SRIPRIYA P	10	9	5	24
38	920816104040	SUNDARAM S K K	10	9	5	24
39	920816104041	SURYA S	10	9	5	24
40	920816104043	TAMILSELVAN M	10	9	5	24
41	920816104044	YOGESH JOHN	10	10	5	25
42	920816104301	SHARUKKHAN S	10	9	5	24
43	920816104701	KISHORE KUMAR D	10	8	5	23
44	920816104901	NAVRIN BHAJILA A	10	10	5	25

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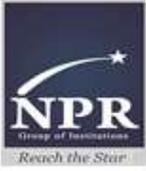
DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

YEAR/ SEM: IV/08

SUB.CODE & NAME: CS6811 & PROJECT WORK

PROJECT REVIEW CONSOLIDATED MARKS

S.NO	REGISTER NUMBER	NAME	ZEROTH REVIEW	1ST REVIEW	2ND REVIEW	3RD REVIEW	FINAL MARKS
1	920816104001	ABIRAMI M	25	25	24	25	99
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3	920816104003	ASHWIN SARAVANAN RC	23	22	23	23	91
4	920816104004	BALAJI J	23	24	24	23	94
5	920816104005	BALAJI R M	22	23	22	24	91
6	920816104006	CHARUMATHY B	25	24	24	24	97
7	920816104008	DIVYA A	25	24	25	25	99
8	920816104009	DURGADEVI P	24	25	24	25	98
9	920816104010	GOMATHI AKILA D	24	25	24	25	98
10	920816104011	GURU PRASATH S	23	24	23	25	95
11	920816104012	HARI VIGNESH S	25	25	24	25	99
12	920816104013	ILAMATHI G	24	25	25	25	99
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15	920816104016	KARTHIGA S	24	24	24	24	96
16	920816104017	KEERTHANA M	25	24	25	25	99
17	920816104018	MADHAN N	22	23	24	24	93
18	920816104019	MADHUMITHA A	25	24	24	25	98
19	920816104020	MAHALAKSHMI M	23	23	24	25	95
20	920816104021	MOINUDEEN S	23	23	24	25	95
21	920816104022	MUTHU T	25	25	24	24	98
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23	920816104024	PAVITHRAN M	23	23	23	24	93
24	920816104025	PRASATH T	24	24	24	24	96
25	920816104026	PRAVIN VINO M	24	25	23	23	95
26	920816104027	PREETHA B	25	23	24	25	97
27	920816104028	PREETHA T	25	25	25	24	99
28	920816104029	SABARI KRISHNAN S K	25	24	24	25	98
29	920816104030	SANTHOSH KUMAR A	23	24	23	25	95
30	920816104031	SELVANAYAKI C	25	25	25	24	99
31	920816104032	SENTHIL N P	24	25	24	25	98



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32	920816104033	SHREEDHAR V	24	25	25	25	99
33	920816104034	SHRI JANANI S	25	24	24	25	98
34	920816104035	SHRUTHI M	24	25	24	25	98
35	920816104036	SIVASUBRAMANIAN C	24	24	24	24	96
36	920816104038	SRINITHI P	25	24	24	25	98
37	920816104039	SRIPRIYA P	25	25	25	24	99
38	920816104040	SUNDARAM S K K	24	24	24	24	96
39	920816104041	SURYA S	24	24	24	24	96
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41	920816104044	YOGESH JOHN	24	24	24	25	97
42	920816104301	SHARUKKHAN S	24	25	25	24	98
43	920816104701	KISHORE KUMAR D	24	22	22	23	91
44	920816104901	NAVRIN BHAJILA A	24	25	25	25	99

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